

SHEET **TITLE** *Revision 1.0*

01	COVER SHEET
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05	CPU_LGA1150-B
06	CPU_LGA1150-C
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08	DDR III CHANNEL B 1,2
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16	PCI Express *1 SLOT
17	ITE 8620 LPC IO
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SHEET	TITLE
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<i>Gigabyte Technology</i>			
Cover Sheet			
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Revision 1.0

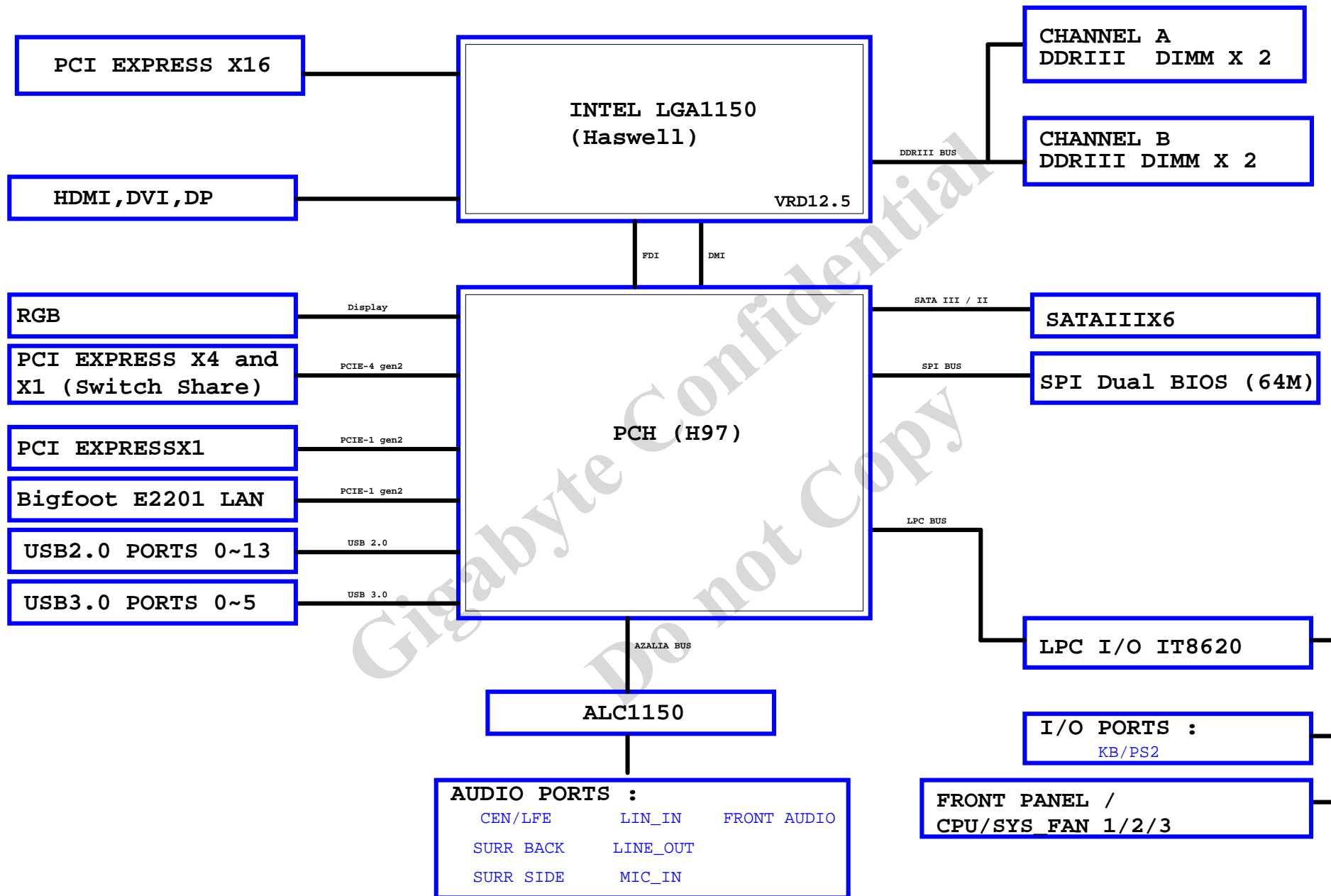
Component value change history

Project Code : U14012-0
Tip/Top : 9MH97MG3-00

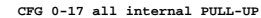
Circuit or PCB layout change

[illegible][illegible]

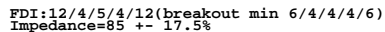
BLOCK DIAGRAM



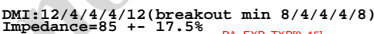
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(D)

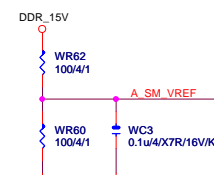


(c)



PA_EXP_TXP[0..15] >> PA_EXP_TXP[0..15] [14]
PA_EXP_TXN[0..15] >> PA_EXP_TXN[0..15] [14]
PA_EXP_RXP[0..15] >> PA_EXP_RXP[0..15] [14]
PA_EXP_RXN[0..15] >> PA_EXP_RXN[0..15] [14]

CPU SVID



THRMTrip DISABLE



Title			
CPU LGA1150-A			
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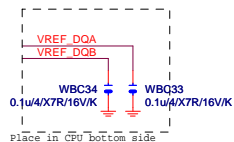
LGA1150 (A)

LGA1150A									
MAAA0	AU13	DDR0_MA0	DDR0_D00	AD38	MDA0				
MAAA1	AV16	DDR0_MA1	DDR0_D01	AD39	MDA1				
MAAA2	AU16	DDR0_MA2	DDR0_D02	AF38	MDA2				
MAAA3	AW17	DDR0_MA3	DDR0_D03	AF39	MDA3				
MAAA4	AU17	DDR0_MA4	DDR0_D04	AD37	MDA4				
MAAA5	AW18	DDR0_MA5	DDR0_D05	AD40	MDA5				
MAAA6	AV17	DDR0_MA6	DDR0_D06	AE37	MDA6				
MAAA7	AT18	DDR0_MA7	DDR0_D07	AF40	MDA7				
MAAA8	AU18	DDR0_MA8	DDR0_D08	AH40	MDA9				
MAAA9	AT19	DDR0_MA9	DDR0_D09	AH39	MDA10				
MAAA10	AW11	DDR0_MA10	DDR0_D10	AK38	MDA10				
MAAA11	AV19	DDR0_MA11	DDR0_D11	AK39	MDA11				
MAAA12	AU19	DDR0_MA12	DDR0_D12	AH37	MDA12				
MAAA13	AY10	DDR0_MA13	DDR0_D13	AH38	MDA14				
MAAA14	AT20	DDR0_MA14	DDR0_D14	AK37	MDA15				
MAAA15	AU21	DDR0_MA15	DDR0_D15	AK40	MDA17				
MODT_A0	AW10	DDR0_ODT0	DDR0_D16	AM40	MDA17				
MODT_A1	AY8	DDR0_ODT1	DDR0_D17	AM39	MDA21				
MODT_A2	AW9	DDR0_ODT2	DDR0_D18	AP38	MDA18				
MODT_A3	AU8	DDR0_ODT3	DDR0_D19	AP39	MDA19				
			DDR0_D20	AM37	MDA20				
			DDR0_D21	AM38	MDA16				
			DDR0_D22	AP37	MDA22				
			DDR0_D23	AP40	MDA23				
			DDR0_D24	AV37	MDA25				
			DDR0_D25	AW37	MDA29				
			DDR0_D26	AU35	MDA28				
			DDR0_D27	AV35	MDA27				
			DDR0_D28	AT37	MDA28				
			DDR0_D29	AU37	MDA24				
			DDR0_D30	AT35	MDA30				
			DDR0_D31	AW35	MDA31				
			DDR0_D32	AY6	MDA33				
			DDR0_D33	AU6	MDA37				
			DDR0_D34	AV4	MDA34				
			DDR0_D35	AU4	MDA36				
			DDR0_D36	AW6	MDA32				
			DDR0_D37	AW4	MDA38				
			DDR0_D38	AY4	MDA39				
			DDR0_D39	AR1	MDA41				
			DDR0_D40	AR4	MDA45				
			DDR0_D41	AN3	MDA42				
			DDR0_D42	AN4	MDA43				
			DDR0_D43	AR2	MDA44				
			DDR0_D44	AR3	MDA40				
			DDR0_D45	AN2	MDA46				
			DDR0_D46	AN1	MDA47				
			DDR0_D47	AL1	MDA49				
			DDR0_D48	AL4	MDA53				
			DDR0_D49	AJ3	MDA50				
			DDR0_D50	AJ4	MDA51				
			DDR0_D51	AL2	MDA52				
			DDR0_D52	AJ2	MDA48				
			DDR0_D53	AJ1	MDA54				
			DDR0_D54	AG1	MDA55				
			DDR0_D55	AG4	MDA61				
			DDR0_D56	AE3	MDA58				
			DDR0_D57	AE4	MDA59				
			DDR0_D58	AG2	MDA60				
			DDR0_D59	AG3	MDA56				
			DDR0_D60	AE2	MDA62				
			DDR0_D61	AE1	MDA63				
			DDR0_D62	AE39	MDA63				
			DDR0_D63	AJ39	DQSA1				
			DDR0_D64	AN39	DQSA2				
			DDR0_D65	AV36	DQSA3				
			DDR0_D66	AV5	DQSA4				
			DDR0_D67	AP3	DQSA5				
			DDR0_D68	AK3	DQSA6				
			DDR0_D69	AF3	DQSA7				
			DDR0_D70	AV32	DQSA7				
			DDR0_D71	AE38	DQSA0				
			DDR0_D72	AN38	DQSA1				
			DDR0_D73	AU36	DQSA2				
			DDR0_D74	AW5	DQSA3				
			DDR0_D75	AP2	DQSA5				
			DDR0_D76	AK2	DQSA6				
			DDR0_D77	AF2	DQSA7				
			DDR0_D78	AU32	DQSA7				

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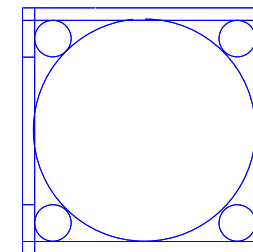
LGA1150 (B)

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MAAB1	AK23	DDR1_MA1	AE35	MDB1					
MAAB2	AM23	DDR1_MA2	AG35	MDB2					
MAAB3	AM23	DDR1_MA3	AH35	MDB3					
MAAB4	AP23	DDR1_MA4	AD34	MDB4					
MAAB5	AL23	DDR1_MA5	AD35	MDB5					
MAAB6	AY24	DDR1_MA6	AG34	MDB6					
MAAB7	AV25	DDR1_MA7	AH34	MDB7					
MAAB8	AU26	DDR1_MA8	AL34	MDB8					
MAAB9	AV25	DDR1_MA9	AL35	MDB9					
MAAB10	AP18	DDR1_MA10	AK31	MDB10					
MAAB11	AY25	DDR1_MA11	AL31	MDB11					
MAAB12	AV26	DDR1_MA12	AK34	MDB12					
MAAB13	AR15	DDR1_MA13	AK35	MDB13					
MAAB14	AV27	DDR1_MA14	AK32	MDB14					
MAAB15	AY28	DDR1_MA15	AL32	MDB15					
MODT_B0	AM17	DDR1_ODT0	AP34	MDB21					
MODT_B1	AL16	DDR1_ODT1	AN31	MDB19					
MODT_B2	AM16	DDR1_ODT2	AP31	MDB23					
MODT_B3	AK15	DDR1_ODT3	AP35	MDB20					
			AP35	MDB16					
			AN32	MDB18					
			AP32	MDB22					
			AP29	MDB25					
			AM28	MDB28					
			AR29	MDB27					
			AR28	MDB30					
			AL28	MDB24					
			AL28	MDB29					
			AP29	MDB26					
			AP28	MDB31					
			AR12	MDB32					
			AP12	MDB33					
			AL13	MDB34					
			AL12	MDB35					
			AP13	MDB36					
			AM13	MDB38					
			AM12	MDB39					
			AR9	MDB45					
			AP9	MDB41					
			AR6	MDB47					
			AP6	MDB43					
			AR10	MDB44					
			AP10	MDB40					
			AR7	MDB46					
			AP7	MDB42					
			AM9	MDB52					
			AL9	MDB53					
			AL6	MDB50					
			AL7	MDB55					
			AM10	MDB48					
			AL10	MDB49					
			AM6	MDB54					
			AM7	MDB51					
			AH6	MDB61					
			AH7	MDB60					
			AE6	MDB59					
			AE7	MDB63					
			AJ6	MDB56					
			AJ7	MDB57					
			AF6	MDB58					
			AF7	MDB62					
			AF36	DQSB0					
			AL33	DQSB1					
			AN28	DQSB2					
			AN12	DQSB3					
			AP8	DQSB5					
			AL8	DQSB6					
			AG7	DQSB7					
			AN25	DQSB7					
			AE34	DQSB0					
			AK33	DQSB1					
			AN33	DQSB2					
			AN29	DQSB3					
			AL13	DQSB4					
			AR8	DQSB5					
			AM8	DQSB6					
			AG6	DQSB7					
			AN26	DQSB7					



HASWELL[10SC1-F01150-01R_10SC1-F01150-03R]

LGA1150 (CR)

CR
CPU RETAINTIONX

LGA1150



ILM_BP/1156/CSP/LM_BP/1156/CSP/[12KRC-OF0001-52R_12KRC-OF0001-51R]

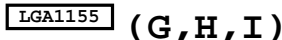
DDR BUS

[7]	MODT_A[0..3]	MODT_A[0..3]
[8]	MODT_B[0..3]	MODT_B[0..3]
[7]	MDA[0..63]	MDA[0..63]
[8]	MDB[0..63]	MDB[0..63]
[7]	DQSA[0..7]	DQSA[0..7]
[7]	DQSA[0..7]	DQSA[0..7]
[7]	MAAA[0..15]	MAAA[0..15]
[8]	MAAB[0..15]	MAAB[0..15]
[8]	DQSB[0..7]	DQSB[0..7]
[8]	DQSB[0..7]	DQSB[0..7]

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CPU LGA1150-B			
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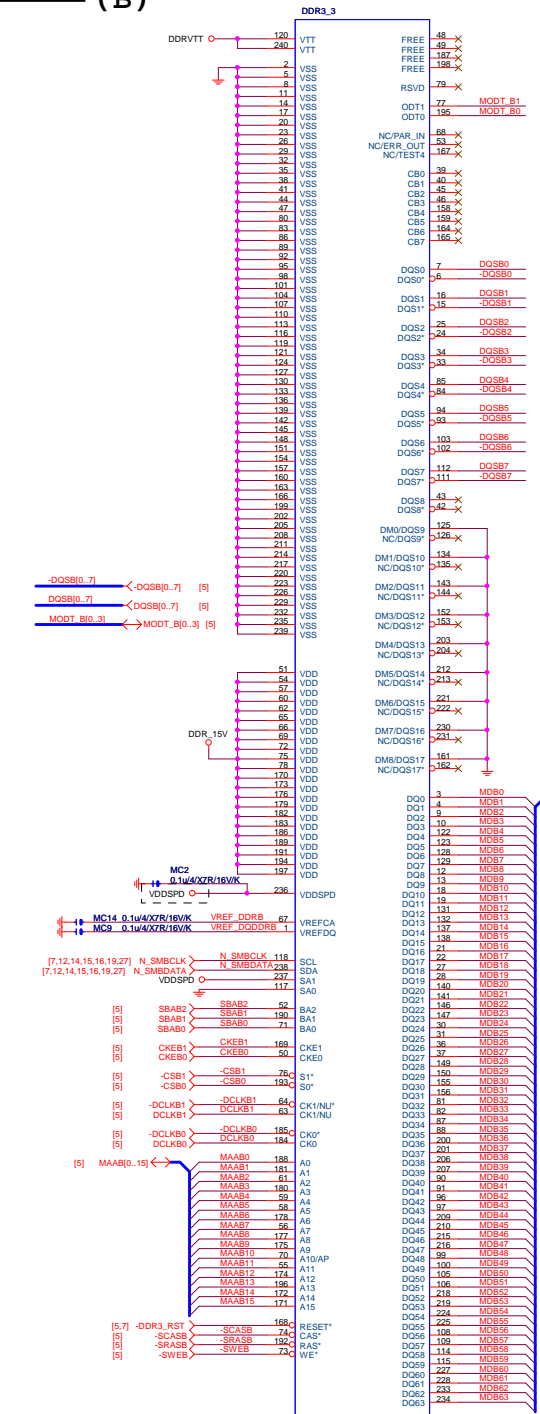
(F, J)



Title			
CPU LGA1150-C			
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DDR3

(B)



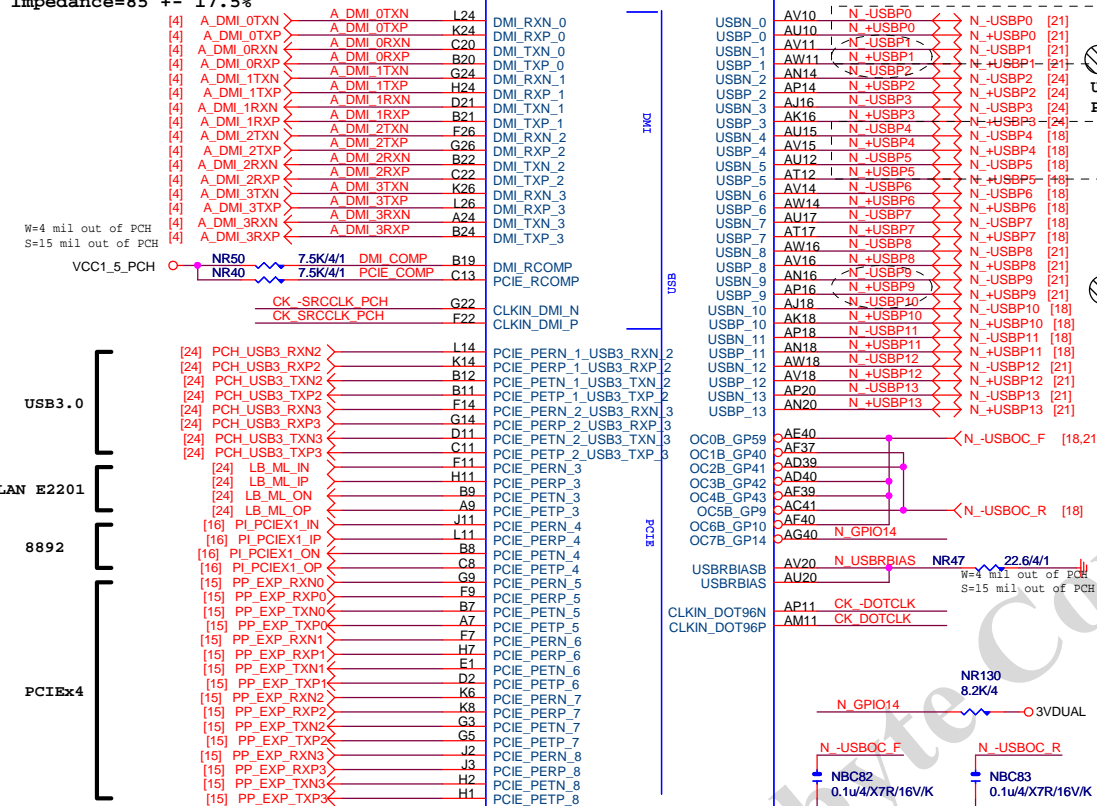
PORT1,PORT9[DEBUG PORT]FOR WHQL一定要拉出PORT

PCH (F)

DMI:12/4/4/4/12(breakout min 8/4/4/4/8)
Impedance=85 +- 17.5%

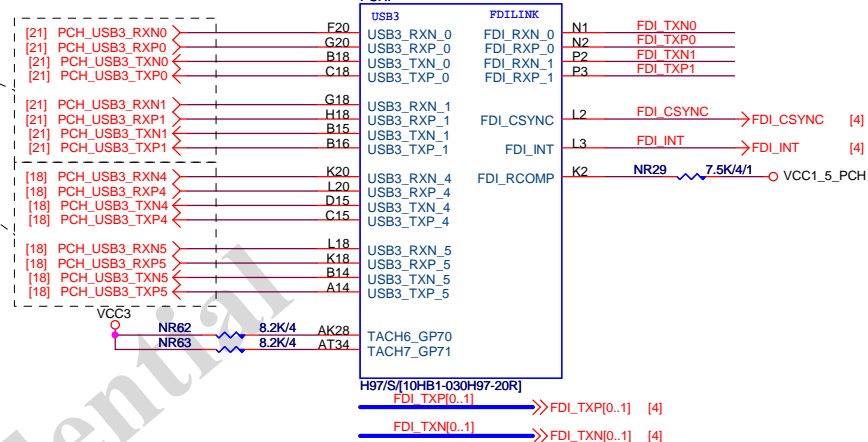
PCHB

B85: Port 6/7 N/A
H81: Port 6/7/12/13 N/A



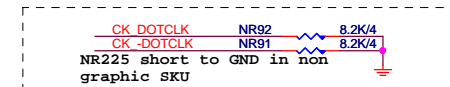
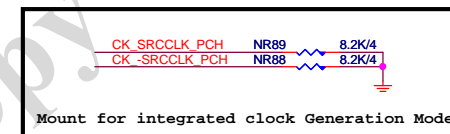
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PCHF



USB3.0:20/5/7/5/20 (breakout min
8/4/4/4/8) ; ONLY 3 VIAS
Impedance=85 +- 17.5%
Back Panel < 10000 MILS
Front Panel < 6000 MILS

PCH	CLK	PD
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PCH (J)

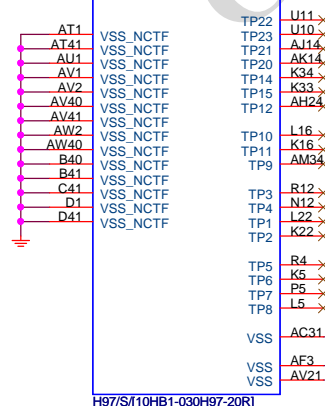
PCH PCIE ,DMI 15/4/4/4//15

```
usb2.0 12/5/7/5//12
usb3.0 20/5/7/5//20
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usb3.0 20/5/7/5//20

usb3.0 20/5/7/5//20

PCHJ

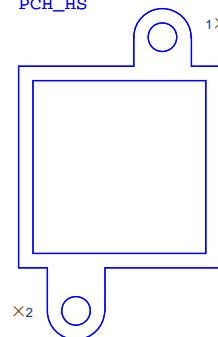


H97/S/10HB1-030H97-20R

PCH H/S

9 Series PCH Heatsink

PCH HS



PCH HS/Z97MX-GAMING 5/12SP2-SG4242-01R 12SP2-SG4242-02R 12SP2-SG4242-03R

USB TABLE

OC[3:0]# for Device 29 (ports 0-7)

OC[7:4]# for Device 26 (ports 8-13)

USB OC#	Configure
OC0#	F_USB30
OC1#	R_USB30
OC2#	USB30_LAN
OC3#	F_USB3
OC4#	F_USB2
OC5#	KB_MS_USB
OC6#	F_USB1
OC7#	Not Use

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PCH FDI,DMI,USB ,PCIE,NVRAM

11

Size	
Custom	

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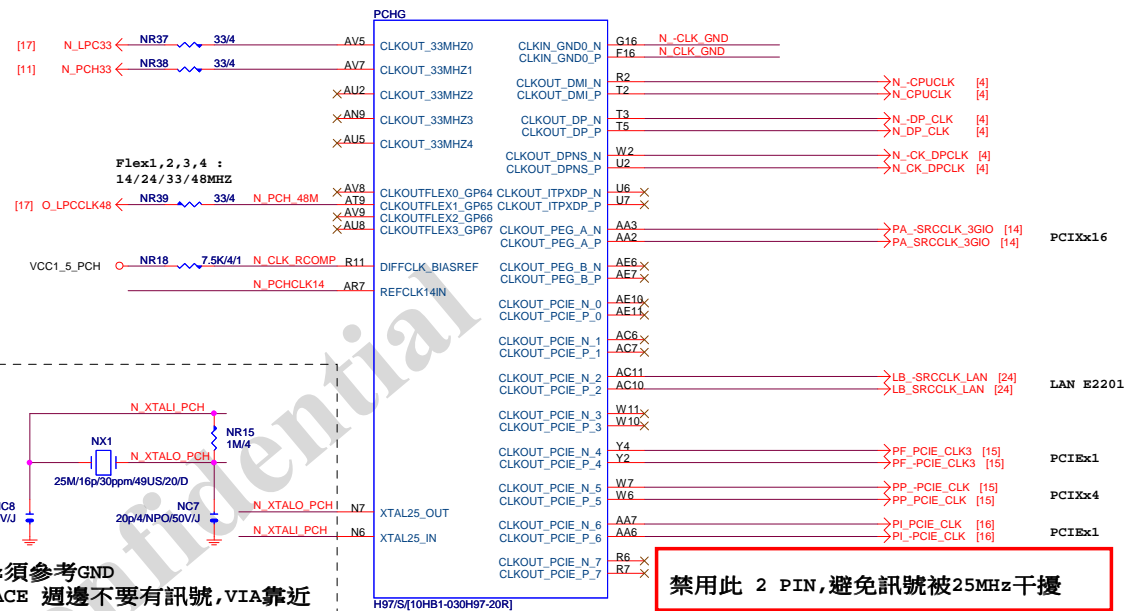
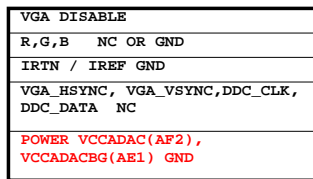
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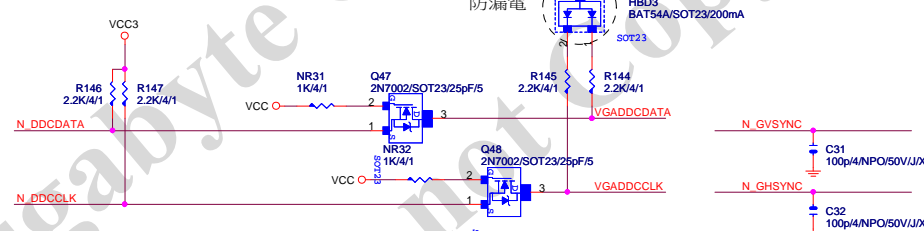
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PCH (G)

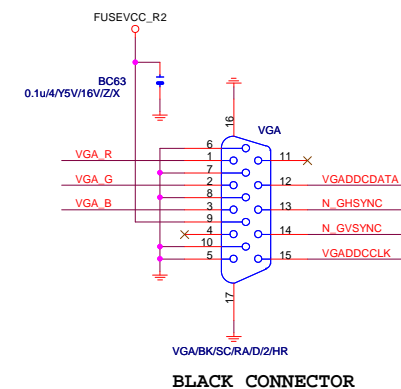


Differential Clock:18/4/6/4/18
Impedance=90 +- 15%

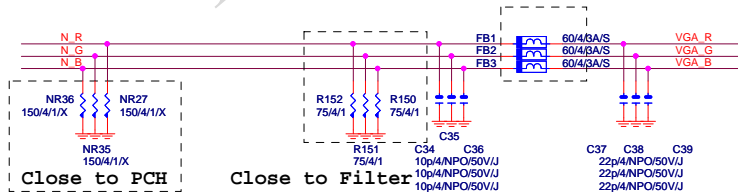
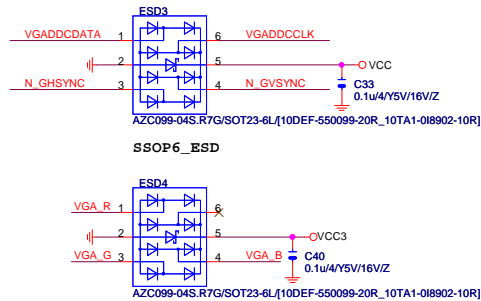
VGA DDC



VGA CONNECTOR



VGA DDC

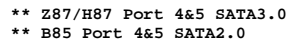


H97 N/A

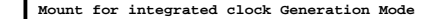
PCHC



N SATA2TXP	0.01u4/X7R/25V/K	NC3
N SATA2TXN	0.01u4/X7R/25V/K	NC3
N SATA2RXN	0.01u4/X7R/25V/K	NC3
N SATA2RXP	0.01u4/X7R/25V/K	NC2



VDUAL PCH



Z97 N/A

Z97 N/A

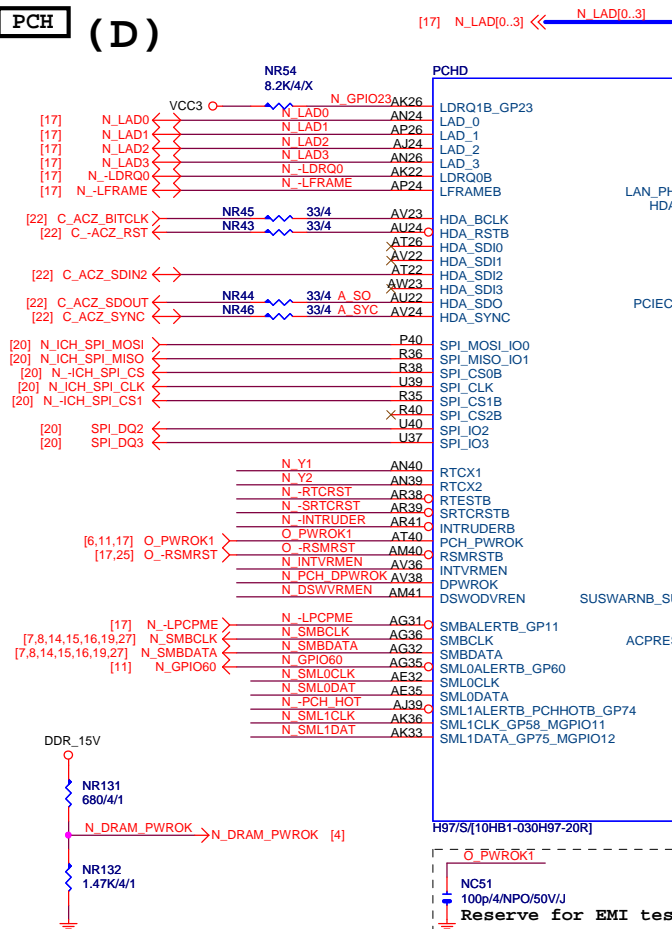


PCH HOST , SATA, PCI

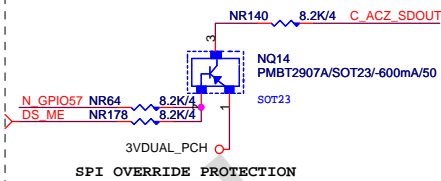
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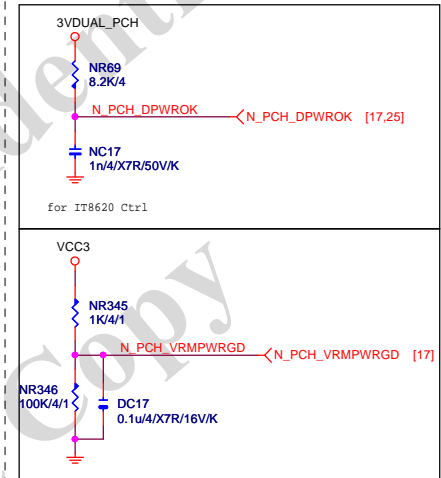
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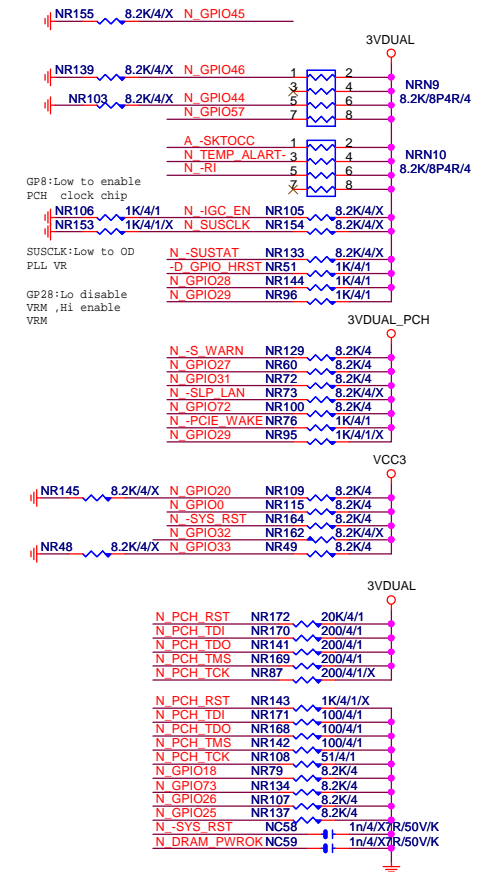
ACZ_SDOUT



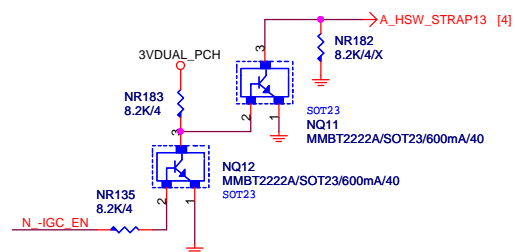
PCH_DPWROK



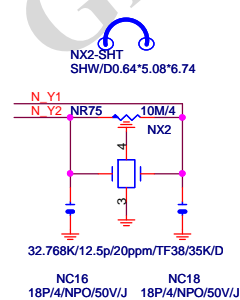
PCH PU/PD



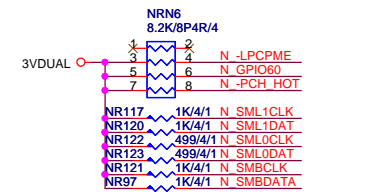
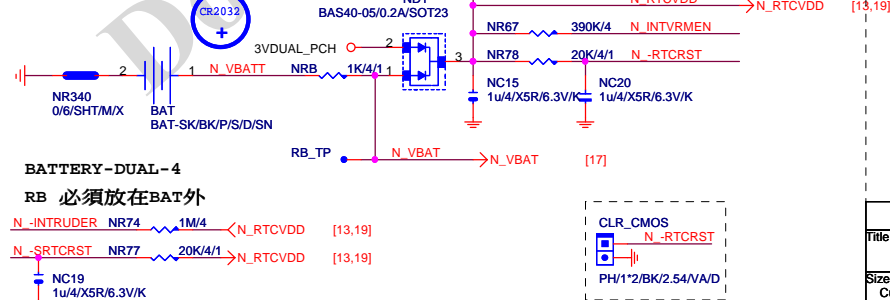
HSW_STRAP13



32.768KHZ



CLR_CMOS

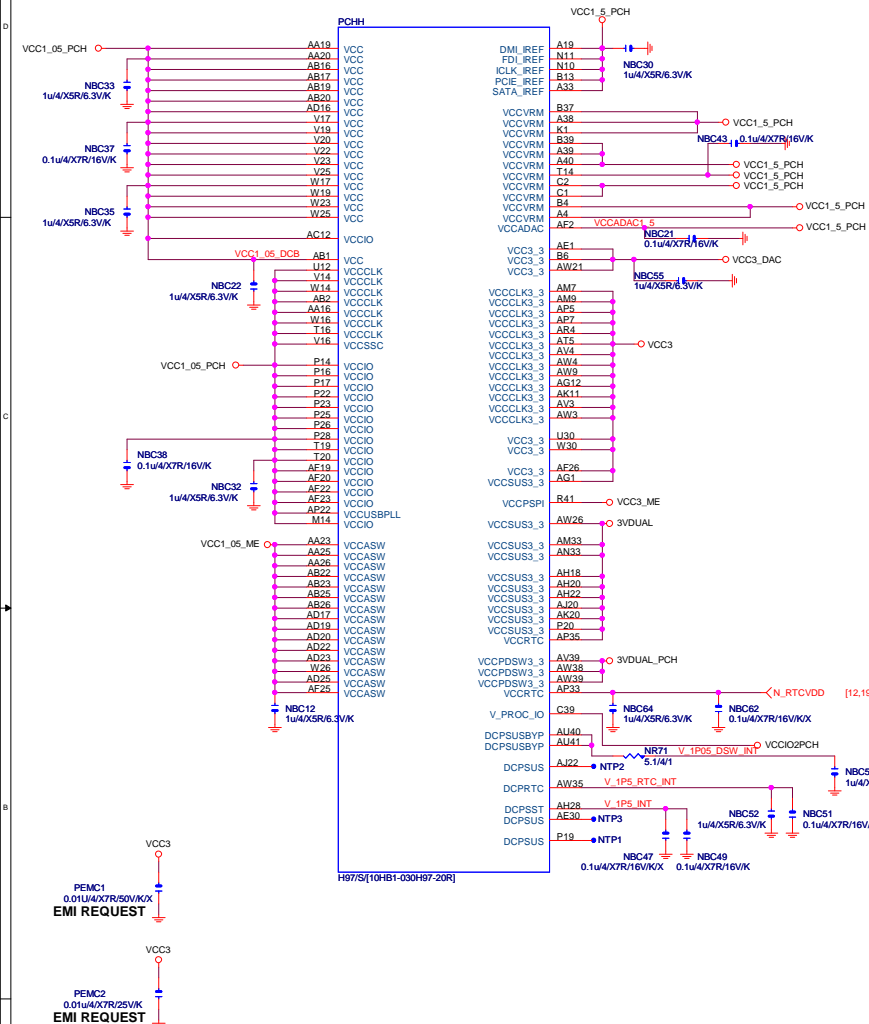


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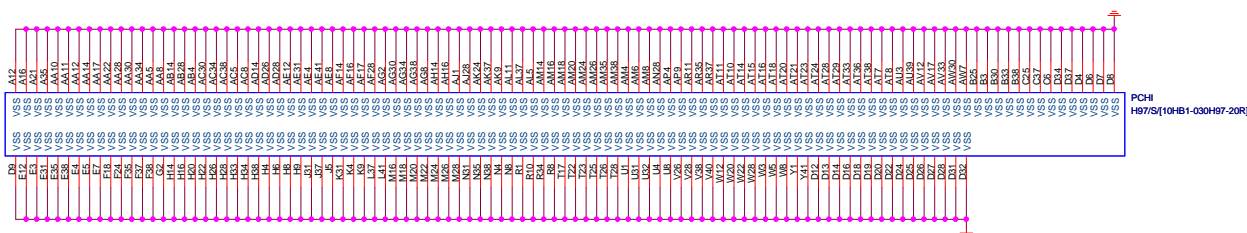
PCH GPIO , CTRL , AUDIO

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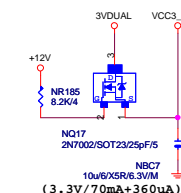
PCH (H)



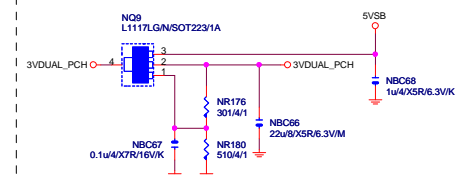
PCH (I)



VCC3_DAC



3VDUAL_PCH



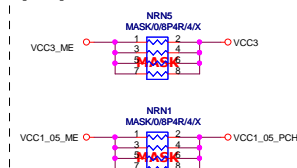
SHT PWR

H97 N/A

R1.0

SHORT WIRE

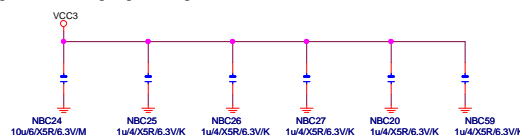
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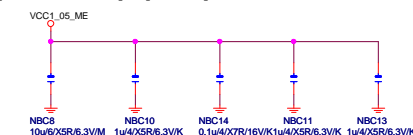
CAP



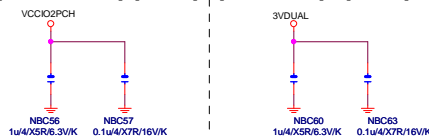
(3.3V) (X6)



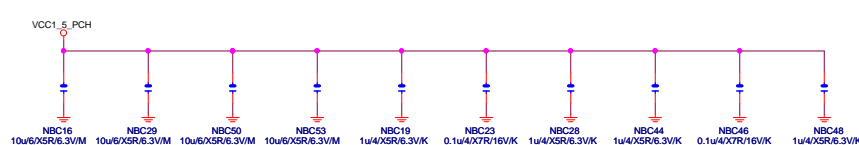
(1.05V) (x5)



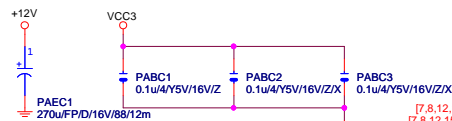
(1.05V) (X6)


$$(1.05V)(x2)(3.3V)(x2)$$


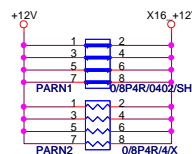
(1.05V) (x10)



PCIEX16 CAP



PCIEX16 PROTECT SHT

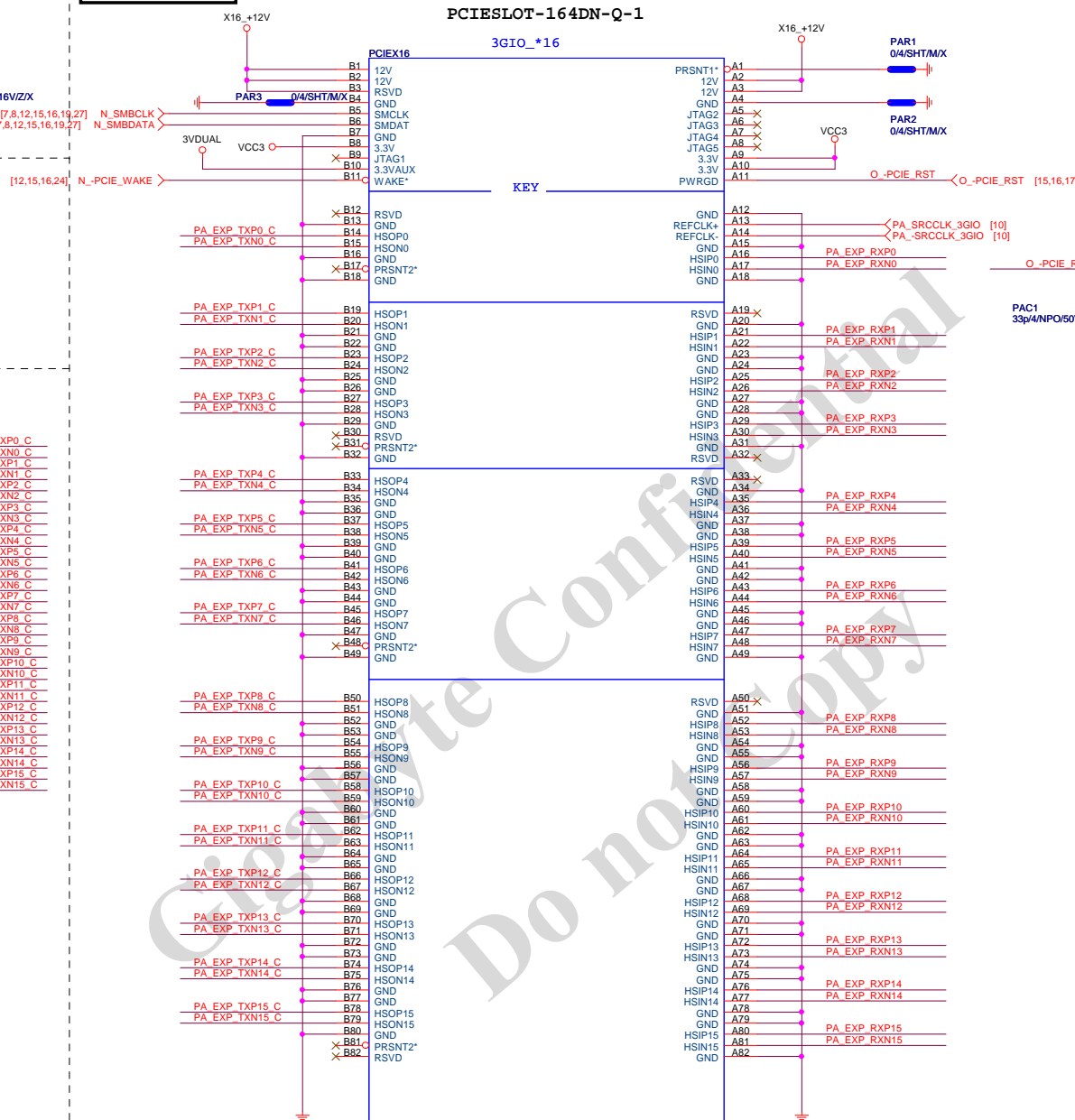


PCIEX16 AC CAP

PA_EXP_TXP0	PAC5	0.22u/4/X5R6.3V/K	PA_EXP_TXP0_C
PA_EXP_TXN0	PAC4	0.22u/4/X5R6.3V/K	PA_EXP_TXN0_C
PA_EXP_TXP1	PAC6	0.22u/4/X5R6.3V/K	PA_EXP_TXP1_C
PA_EXP_TXN1	PAC7	0.22u/4/X5R6.3V/K	PA_EXP_TXN1_C
PA_EXP_TXP2	PAC8	0.22u/4/X5R6.3V/K	PA_EXP_TXP2_C
PA_EXP_TXN2	PAC9	0.22u/4/X5R6.3V/K	PA_EXP_TXN2_C
PA_EXP_TXP3	PAC10	0.22u/4/X5R6.3V/K	PA_EXP_TXP3_C
PA_EXP_TXN3	PAC11	0.22u/4/X5R6.3V/K	PA_EXP_TXN3_C
PA_EXP_TXP4	PAC12	0.22u/4/X5R6.3V/K	PA_EXP_TXP4_C
PA_EXP_TXN4	PAC13	0.22u/4/X5R6.3V/K	PA_EXP_TXN4_C
PA_EXP_TXP5	PAC14	0.22u/4/X5R6.3V/K	PA_EXP_TXP5_C
PA_EXP_TXN5	PAC15	0.22u/4/X5R6.3V/K	PA_EXP_TXN5_C
PA_EXP_TXP6	PAC16	0.22u/4/X5R6.3V/K	PA_EXP_TXP6_C
PA_EXP_TXN6	PAC17	0.22u/4/X5R6.3V/K	PA_EXP_TXN6_C
PA_EXP_TXP7	PAC19	0.22u/4/X5R6.3V/K	PA_EXP_TXP7_C
PA_EXP_TXN7	PAC18	0.22u/4/X5R6.3V/K	PA_EXP_TXN7_C
PA_EXP_TXP8	PAC20	0.22u/4/X5R6.3V/K	PA_EXP_TXP8_C
PA_EXP_TXN8	PAC21	0.22u/4/X5R6.3V/K	PA_EXP_TXN8_C
PA_EXP_TXP9	PAC22	0.22u/4/X5R6.3V/K	PA_EXP_TXP9_C
PA_EXP_TXN9	PAC23	0.22u/4/X5R6.3V/K	PA_EXP_TXN9_C
PA_EXP_TXP10	PAC24	0.22u/4/X5R6.3V/K	PA_EXP_TXP10_C
PA_EXP_TXN10	PAC25	0.22u/4/X5R6.3V/K	PA_EXP_TXN10_C
PA_EXP_TXP11	PAC26	0.22u/4/X5R6.3V/K	PA_EXP_TXP11_C
PA_EXP_TXN11	PAC27	0.22u/4/X5R6.3V/K	PA_EXP_TXN11_C
PA_EXP_TXP12	PAC28	0.22u/4/X5R6.3V/K	PA_EXP_TXP12_C
PA_EXP_TXN12	PAC29	0.22u/4/X5R6.3V/K	PA_EXP_TXN12_C
PA_EXP_TXP13	PAC30	0.22u/4/X5R6.3V/K	PA_EXP_TXP13_C
PA_EXP_TXN13	PAC31	0.22u/4/X5R6.3V/K	PA_EXP_TXN13_C
PA_EXP_TXP14	PAC32	0.22u/4/X5R6.3V/K	PA_EXP_TXP14_C
PA_EXP_TXN14	PAC33	0.22u/4/X5R6.3V/K	PA_EXP_TXN14_C
PA_EXP_TXP15	PAC34	0.22u/4/X5R6.3V/K	PA_EXP_TXP15_C
PA_EXP_TXN15	PAC35	0.22u/4/X5R6.3V/K	PA_EXP_TXN15_C

PA_EXP_RXP[0..15] >>> PA_EXP_RXP[0..15] [4]
 PA_EXP_RXN[0..15] >>> PA_EXP_RXN[0..15] [4]
 PA_EXP_TXP[0..15] >>> PA_EXP_TXP[0..15] [4]
 PA_EXP_TXN[0..15] >>> PA_EXP_TXN[0..15] [4]

PCIEX16 SLOT

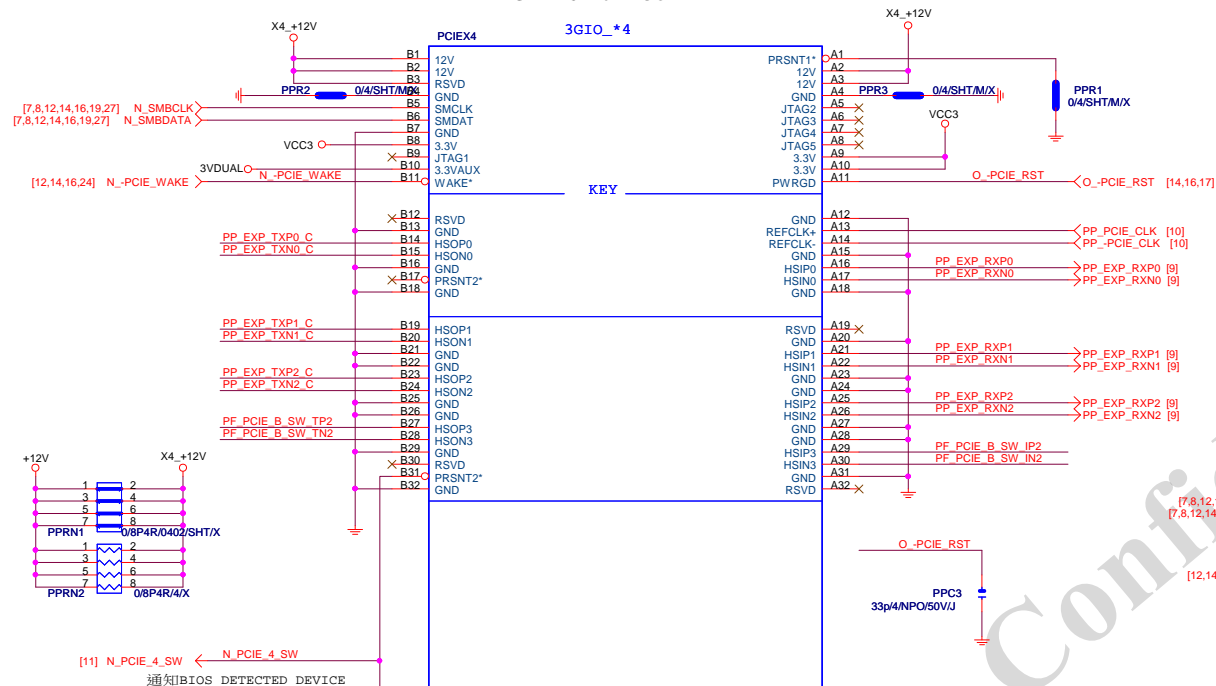


PCI-E/16X-164P/BK/LONG DOUBLE

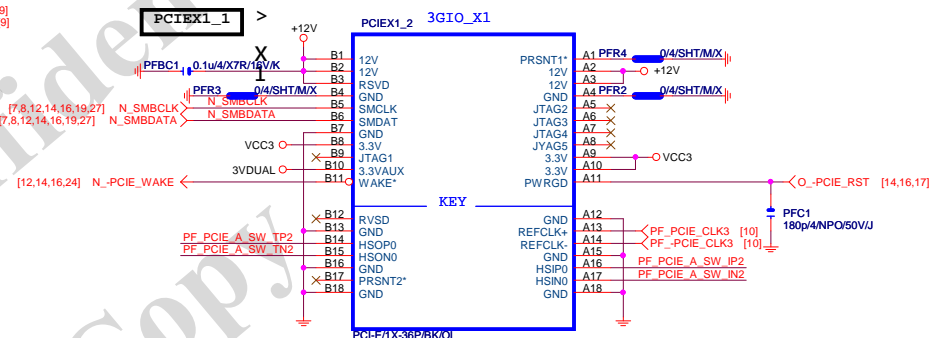
BLACK CONNECTOR

PCIEX4 SLOT

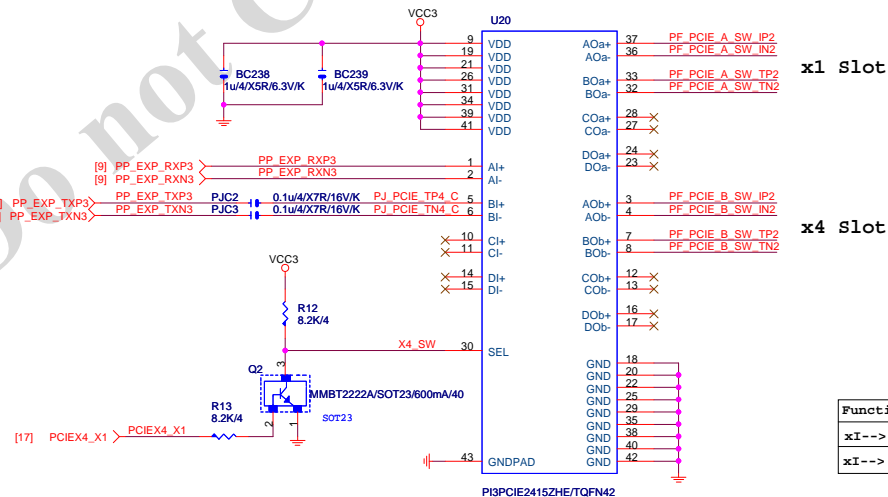
PCIESLOT-64D-98D-P



PCIEX1_1



	N_PCIE_4_SW (PCH GPIO48)	PCIEX4_X1 (SIO GPIO26)
PCIEX4 No devices	H	H
PCIEX4 -> X1	H	H
PCIEX4 Have devices	L	L
PCIEX4 -> X4		
PCIEX1_1/2 --> N/A		

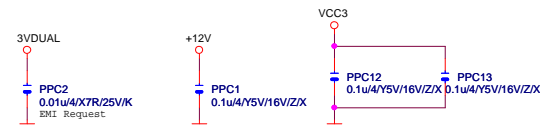


Function	SEL
xI--> x0a	L;PCIEX4 SLOT-->X1
xI--> x0b	H;PCIEX4 SLOT-->X4

PCI-E/4X-65P/BK/LONG DOUBLE

BLACK CONNECTOR

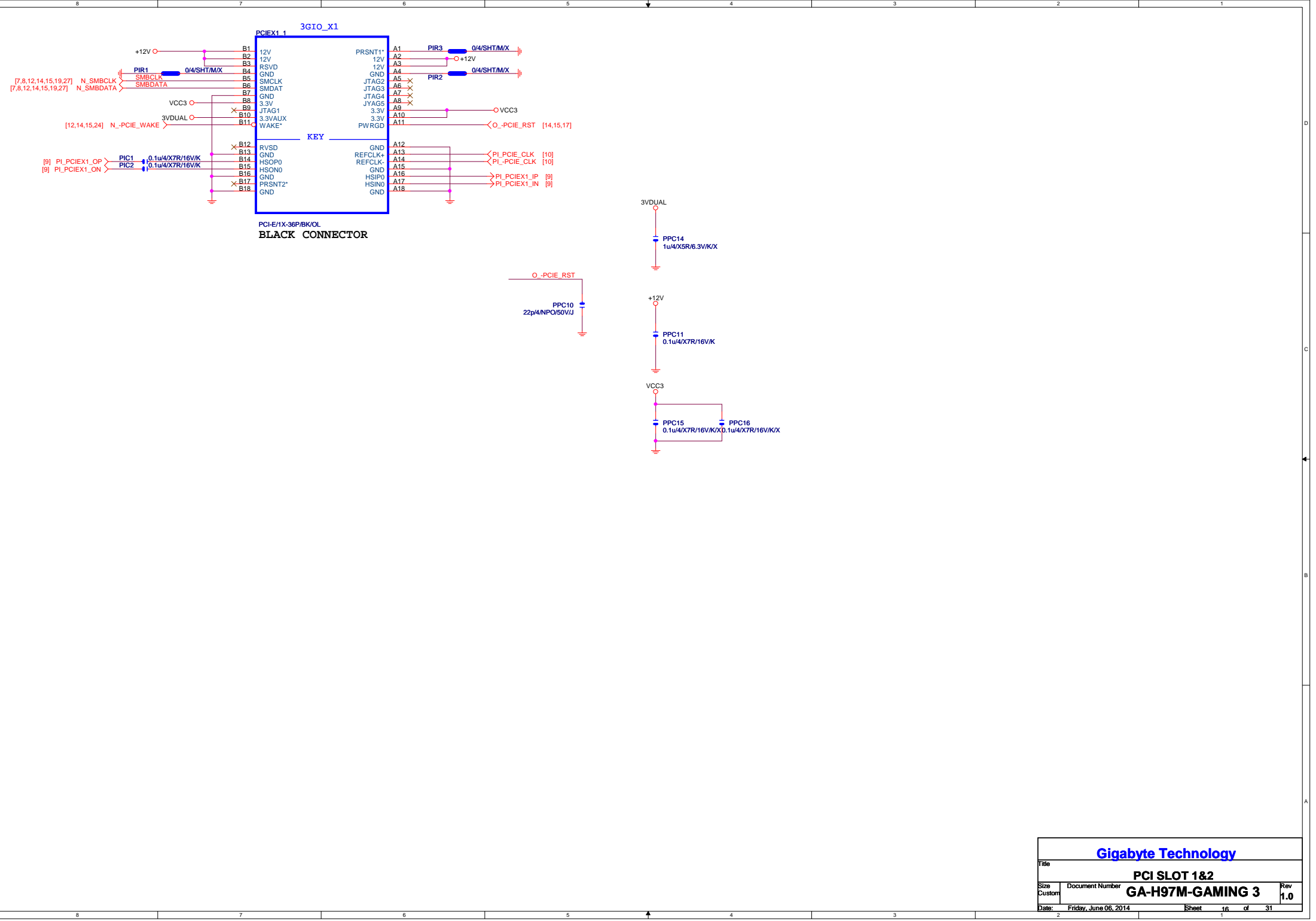
[9] PP_EXP_TXP0	PP_EXP_TXP0	PPC4	0.1u/4X7R/16V/K	PP_EXP_TXP0_C
[9] PP_EXP_TXN0	PP_EXP_TXN0	PPC5	0.1u/4X7R/16V/K	PP_EXP_TXN0_C
[9] PP_EXP_TXP1	PP_EXP_TXP1	PPC6	0.1u/4X7R/16V/K	PP_EXP_TXP1_C
[9] PP_EXP_TXN1	PP_EXP_TXN1	PPC7	0.1u/4X7R/16V/K	PP_EXP_TXN1_C
[9] PP_EXP_TXP2	PP_EXP_TXP2	PPC8	0.1u/4X7R/16V/K	PP_EXP_TXP2_C
[9] PP_EXP_TXN2	PP_EXP_TXN2	PPC9	0.1u/4X7R/16V/K	PP_EXP_TXN2_C



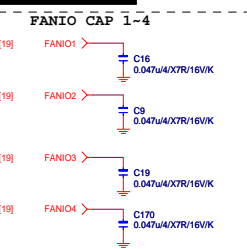
Gigabyte Technology

PCI EXPRESS X 1 PORT

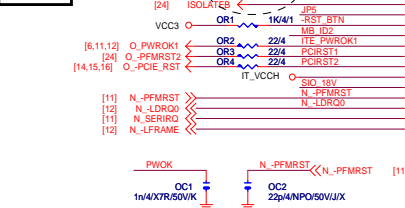
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SIO IT8620

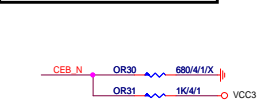


PROCHOT

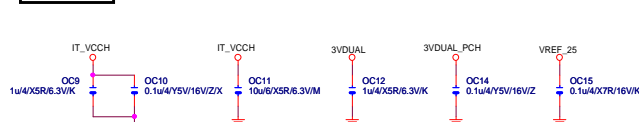


IT8620E GPIO問題匯整	
PIN 50	第一次接上POWER時會拉 LO
PIN 90/91	DEFAULT為HOLD FUNCTION, GP93 BYPASS TO GP92
PIN 108	高溫時 GP92 會被拉LO (ITE BUG)
PIN 111/112	MOUSE 與 FAN FUNCTION 擇一使用, 不然會互相干擾

DUAL BIOS OPT STRAP



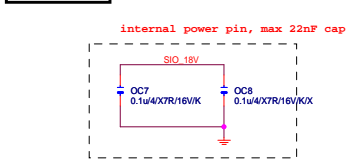
SIO CAP



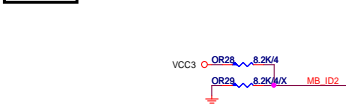
Power leakage



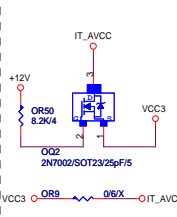
SIO_18V



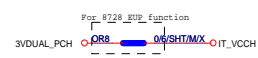
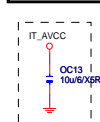
MB ID



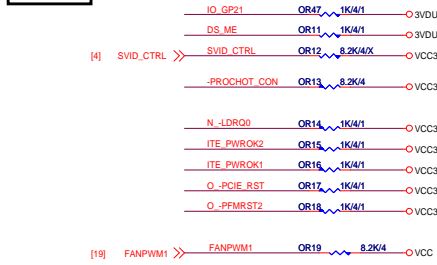
FIX ATX 插拔漏電



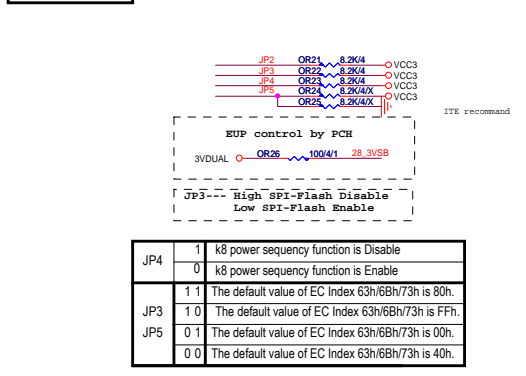
PWR_SHT



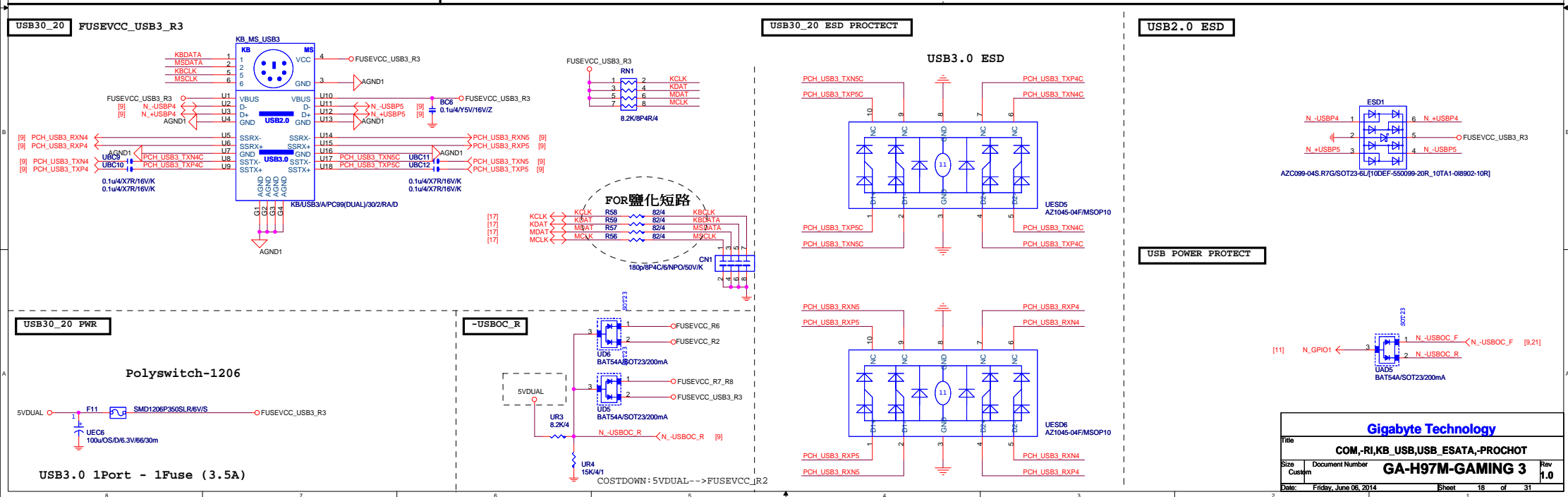
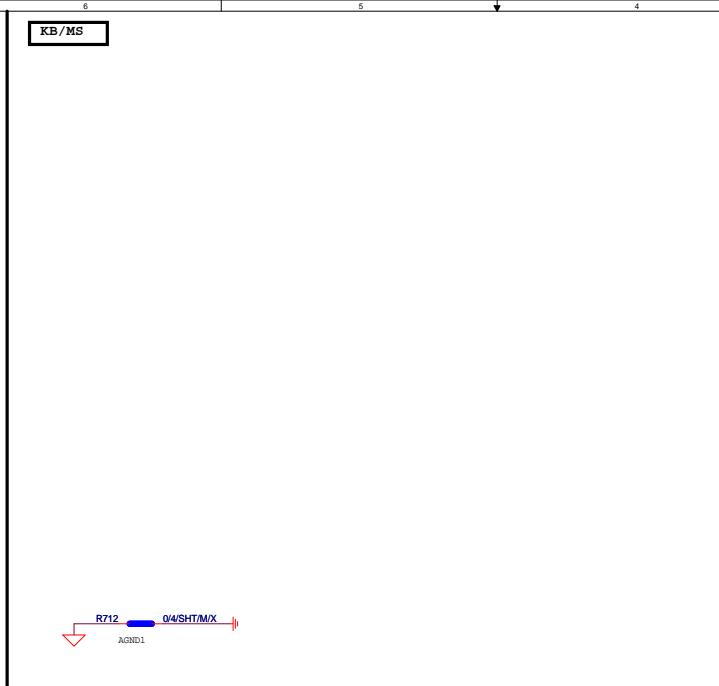
SIO_PU



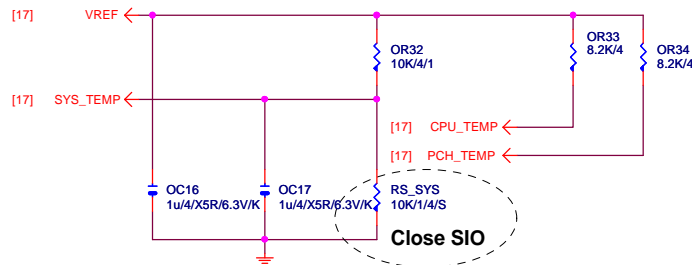
SIO STRAP



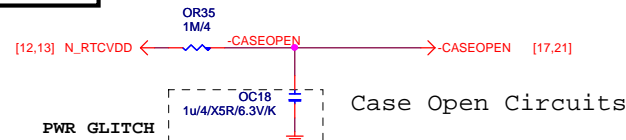
JP4	1	k8 power sequency function is Disable
JP4	0	k8 power sequency function is Enable
JP3	1 1	The default value of EC Index 63h/6Bh/73h is 80h.
JP3	1 0	The default value of EC Index 63h/6Bh/73h is FFh.
JP5	1 0	The default value of EC Index 63h/6Bh/73h is 00h.
JP5	0 0	The default value of EC Index 63h/6Bh/73h is 40h.



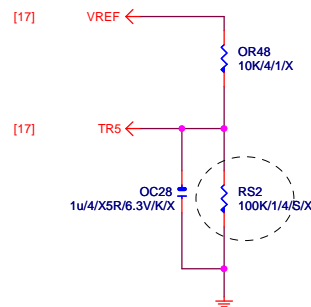
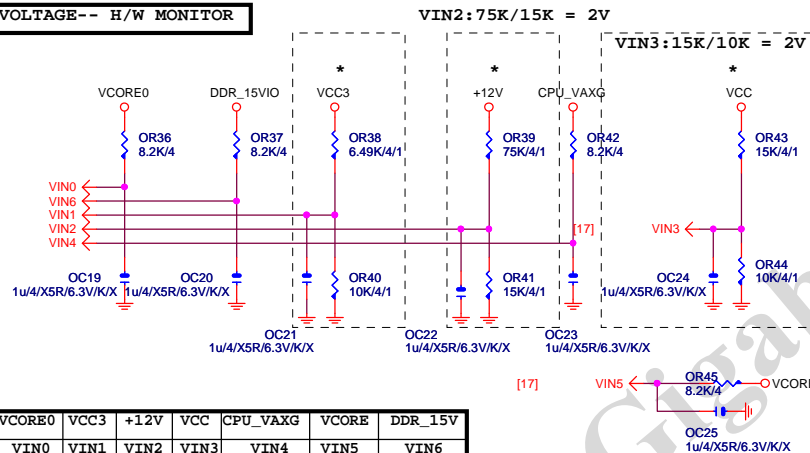
TEMP H/W MONITOR



CASE OPEN



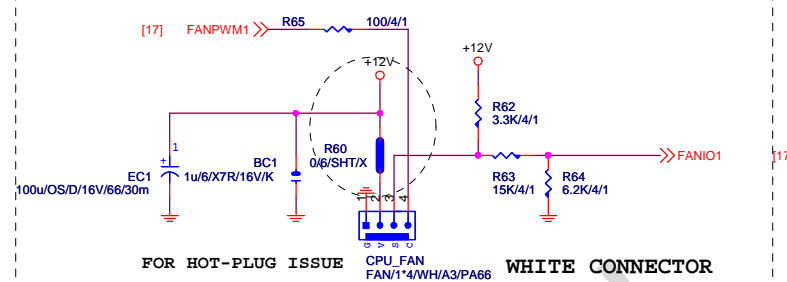
VOLTAGE-- H/W MONITOR



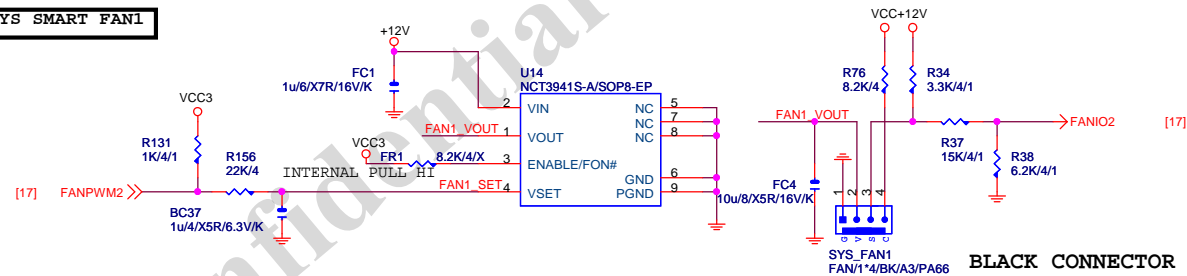
RS2 CLOSE CPU VR MOSFET

RS2 CLOSE MOSFET(VIN): DCQ1

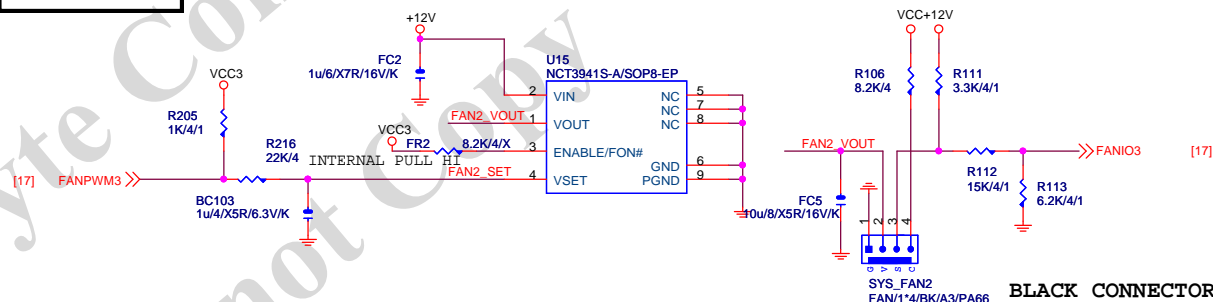
CPU SMART FAN



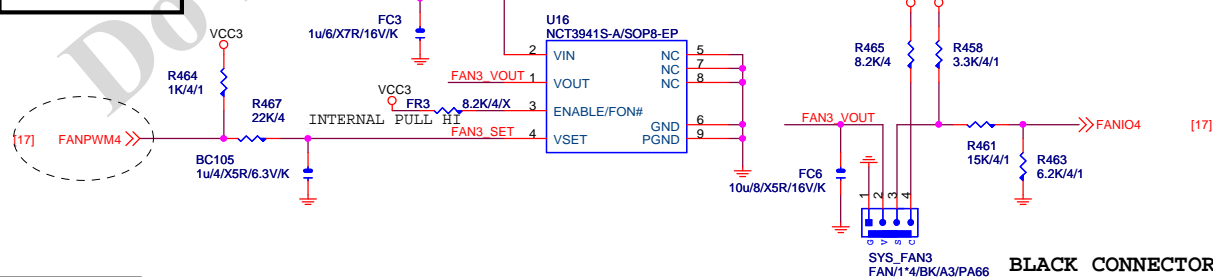
SYS SMART FAN1



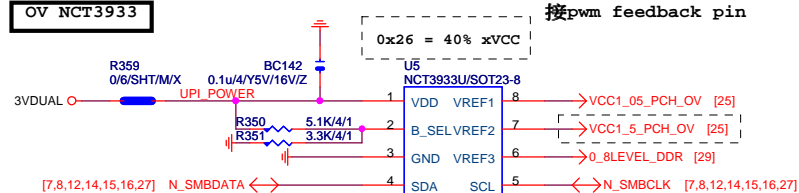
SYS SMART FAN2



SYS SMART FAN3

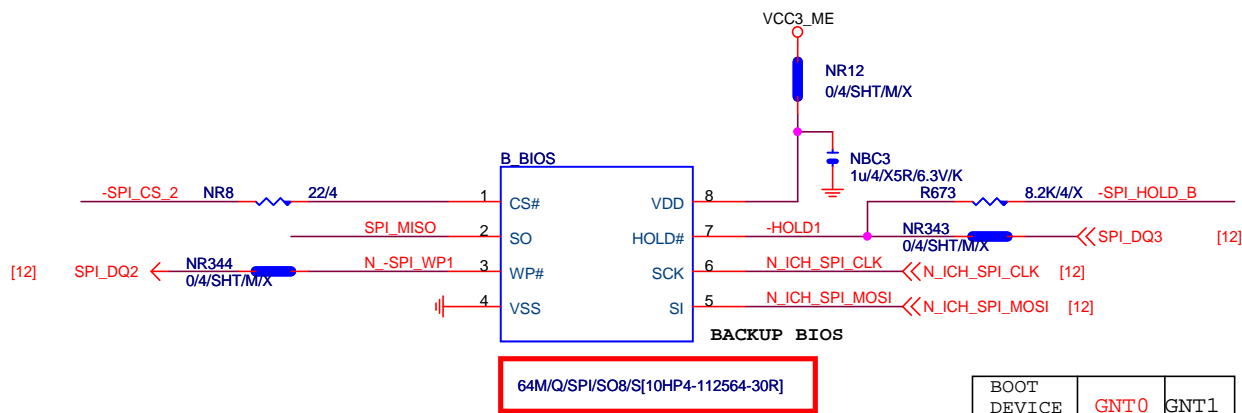
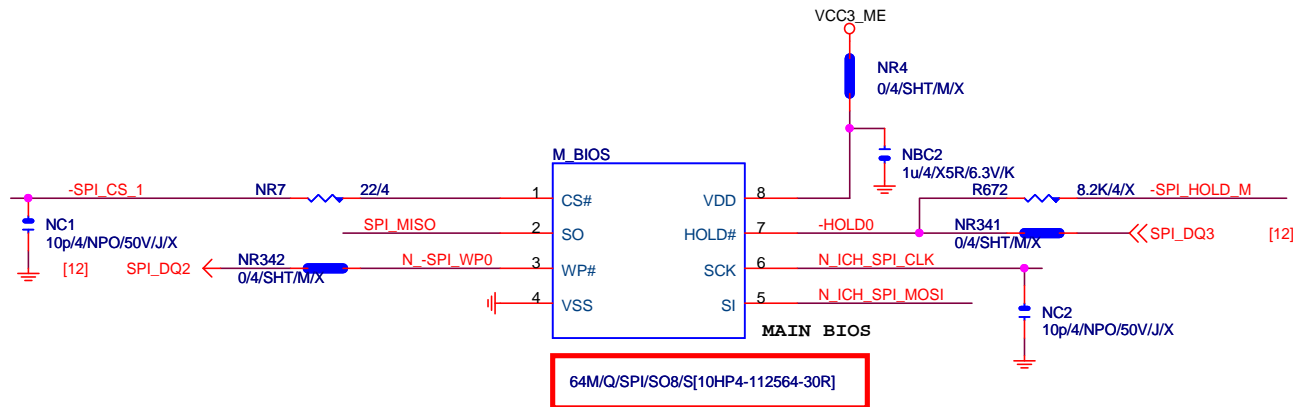


OV NCT3933



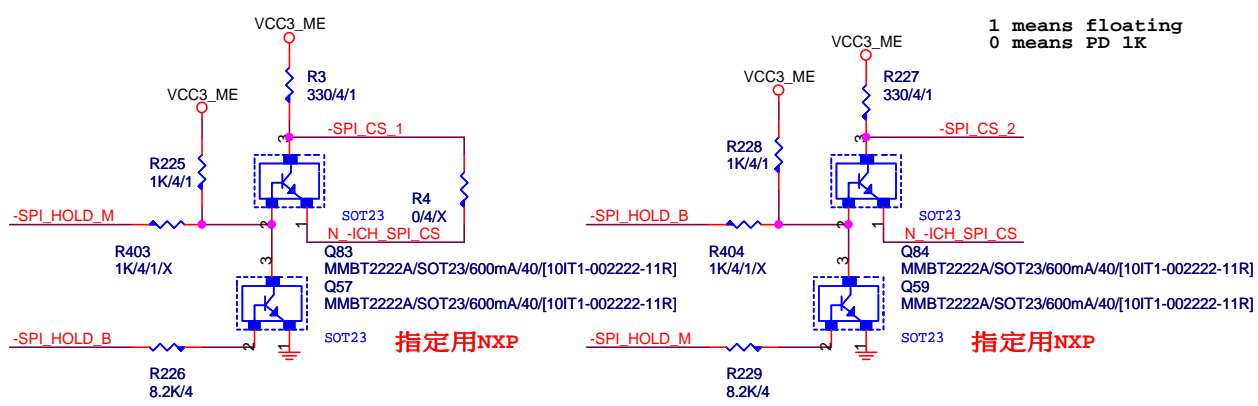
Gigabyte Technology

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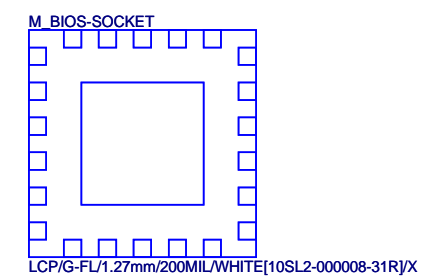
BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating
0 means PD 1K

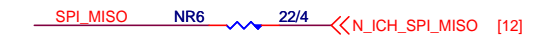
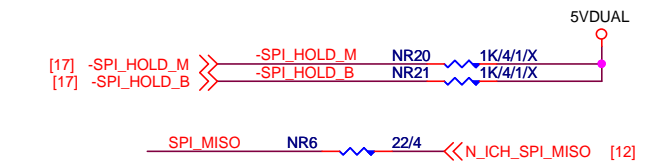
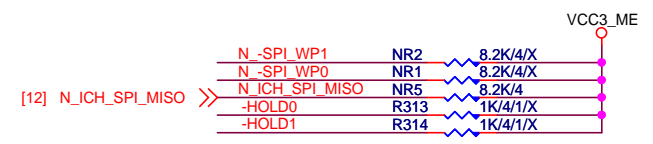


BIOS DEBUG PORT

BIOS_PH R1.0 移除



MOSI For DMI RX Termination Voltage



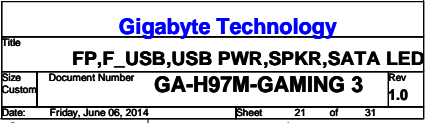
Gigabyte Technology

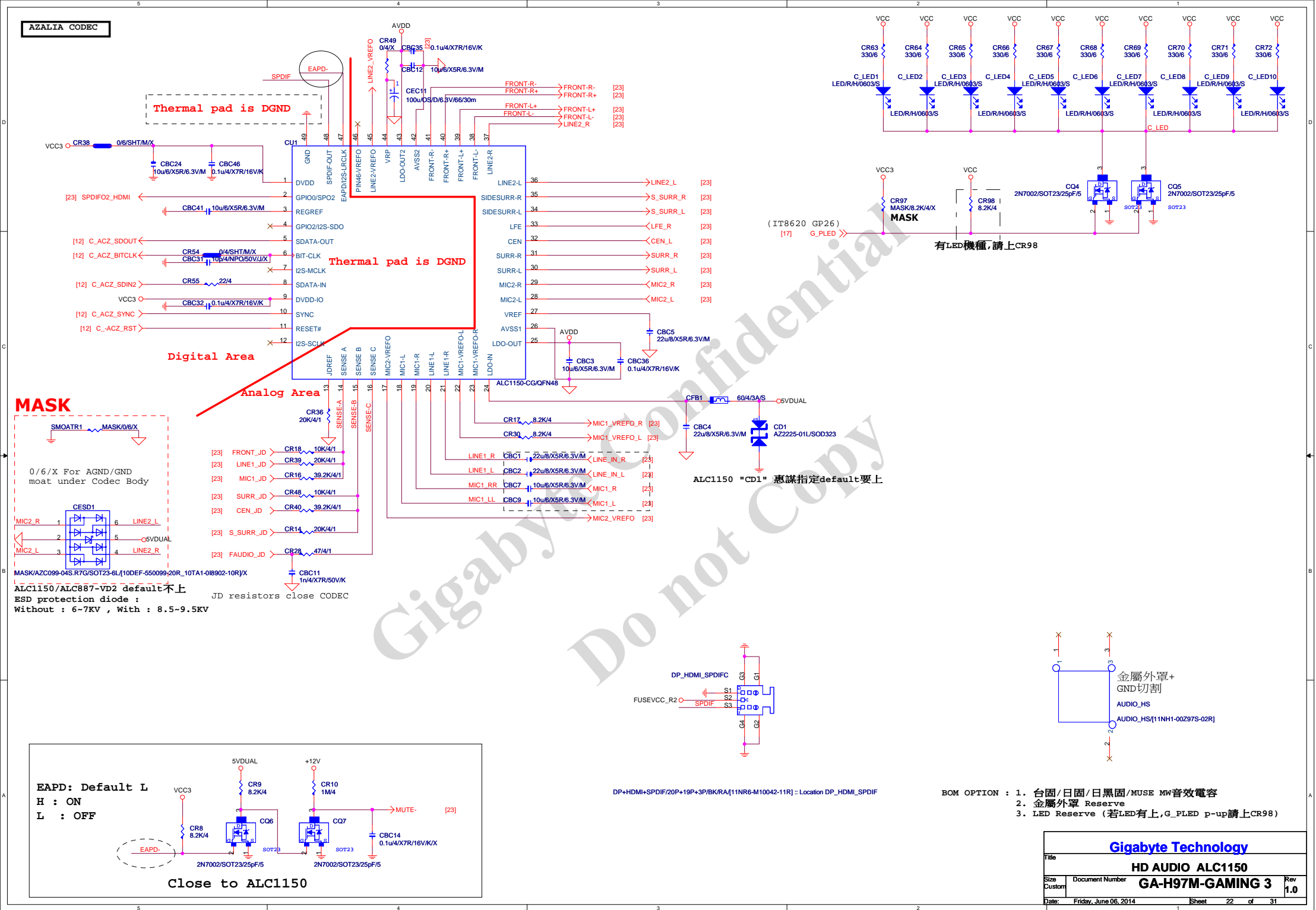
DUAL BIOS

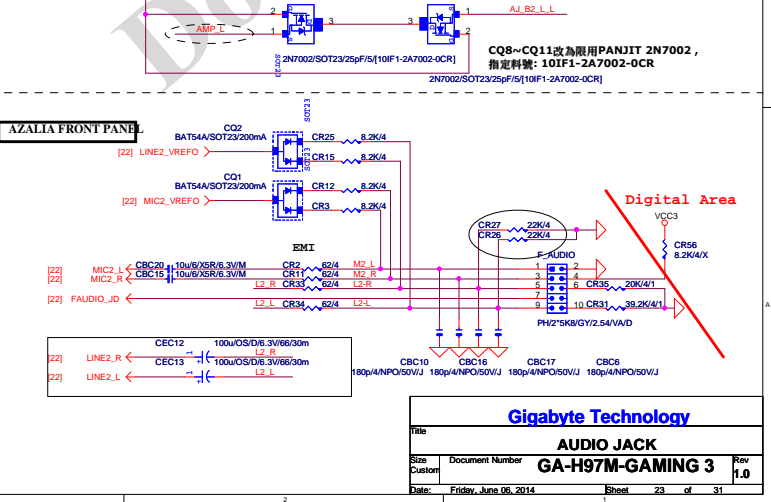
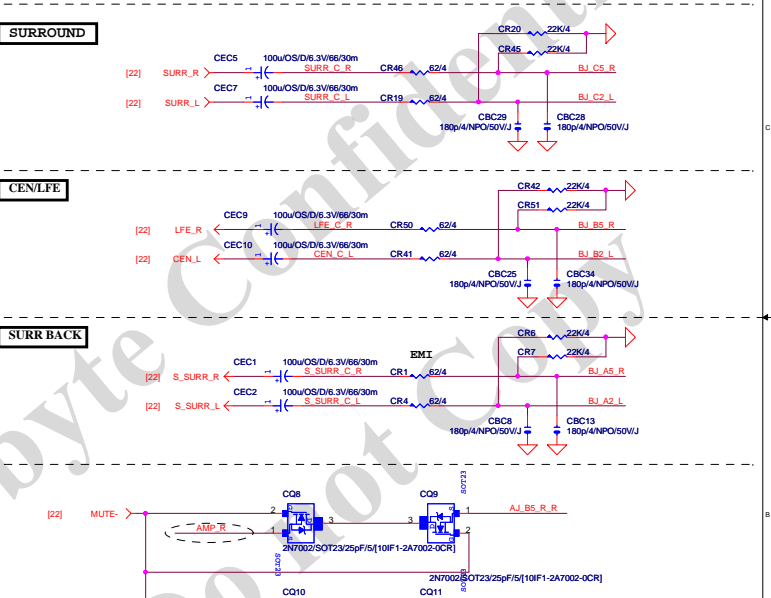
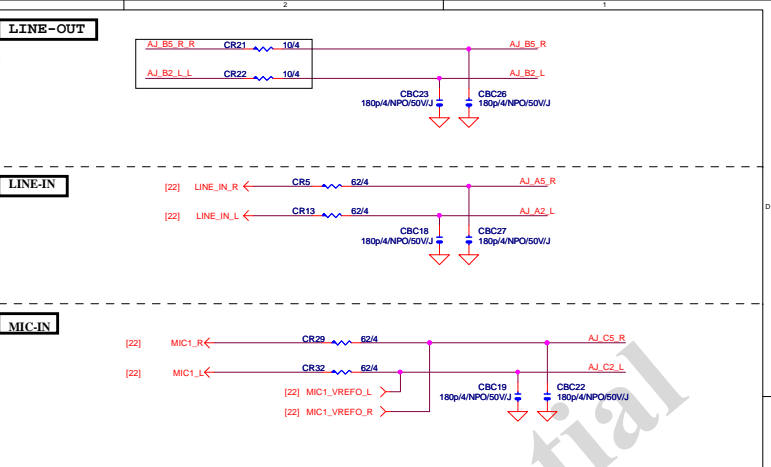
GA-H97M-GAMING 3

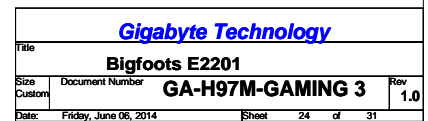
Rev 1.0

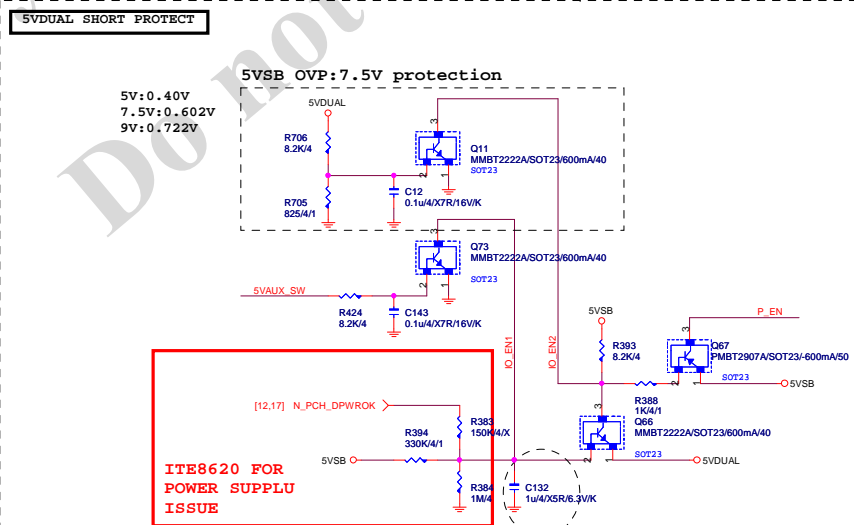
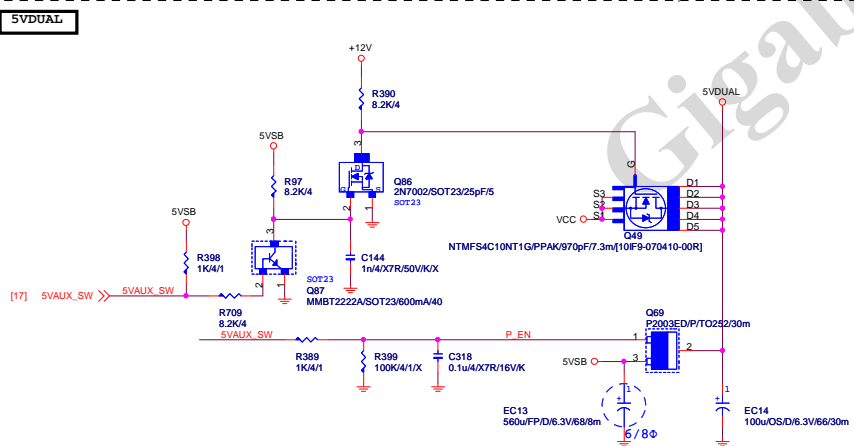
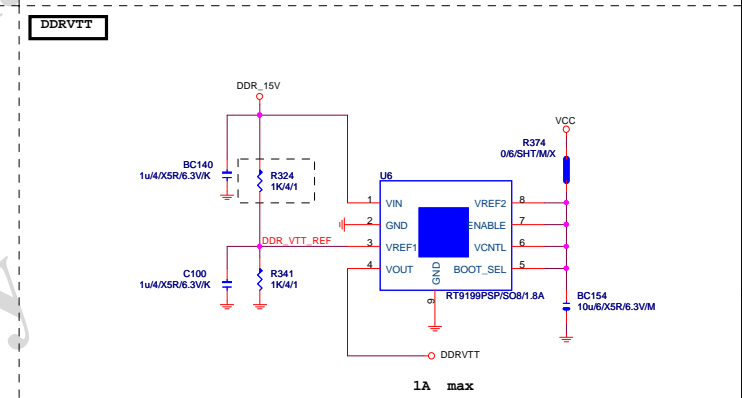
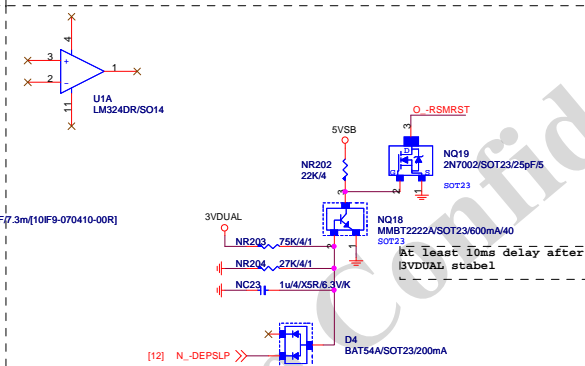
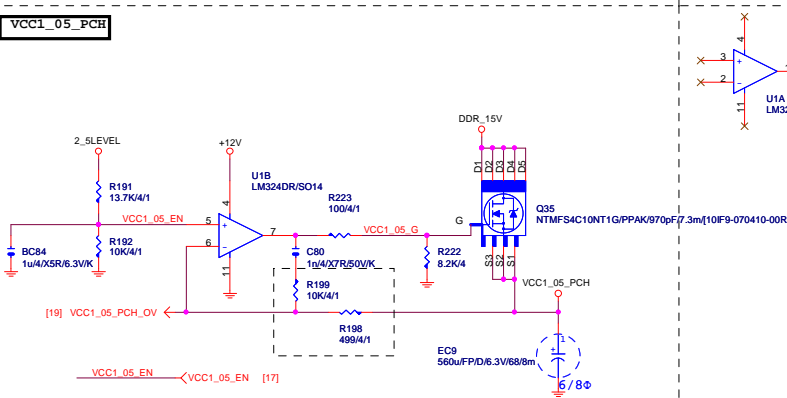
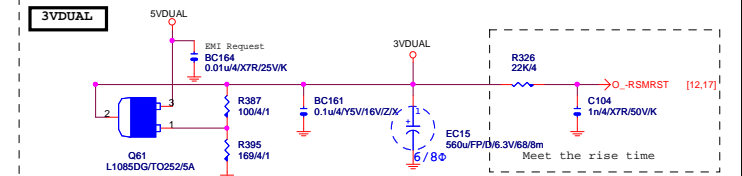
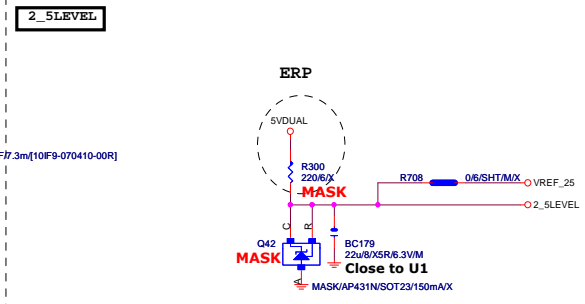
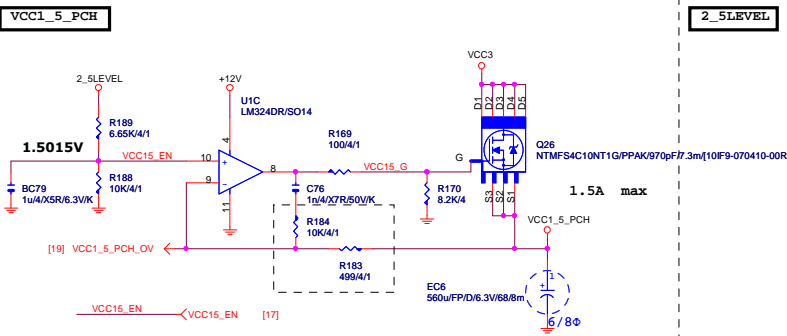
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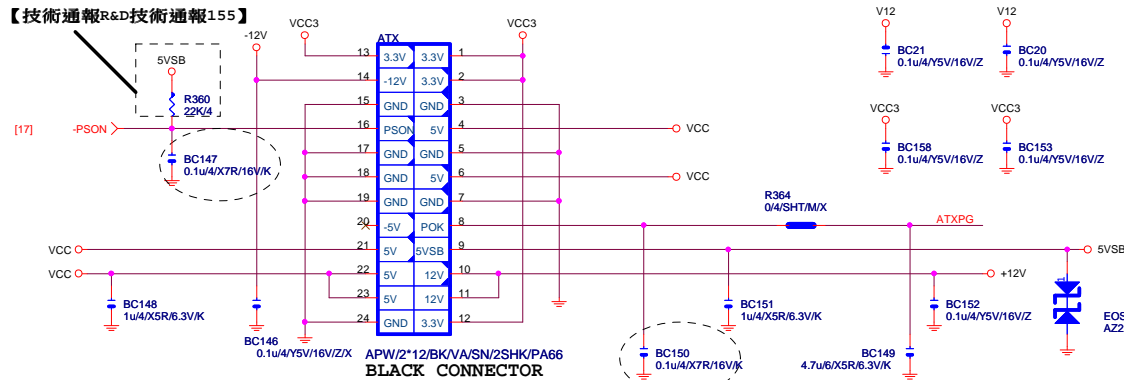






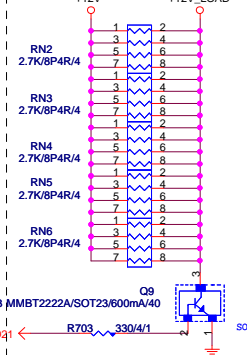
ATXX24 POWER CONNECTOR

【技術通報R&D技術通報155】



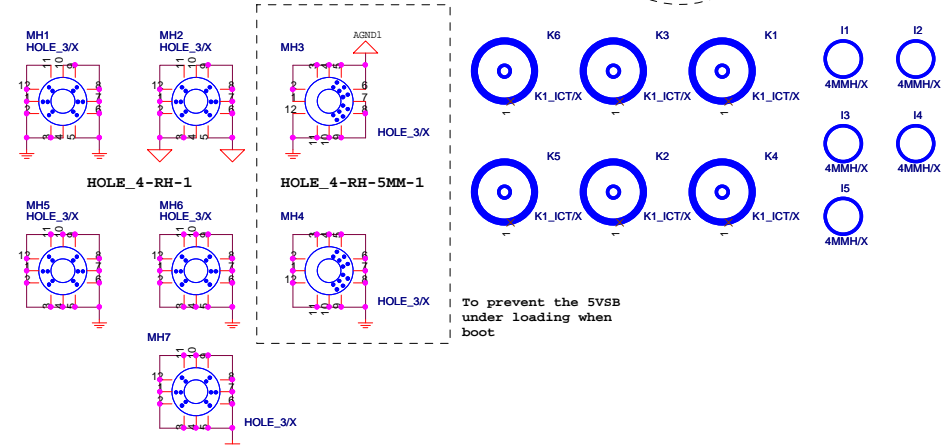
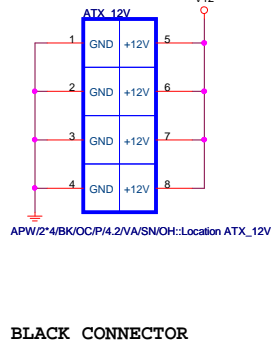
【技術通報R&D技術通報153】

To fix 12V light load abnormal issue



ATXX4 POWER CONNECTOR

【技術通報R&D技術通報153】

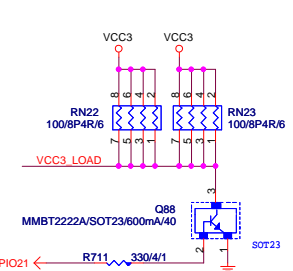


TPM



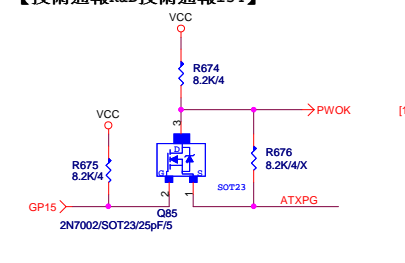
To prevent the 5VSB under loading when boot

FIX PWR MINMUN LOAD



PWOK PATCH

【技術通報R&D技術通報154】

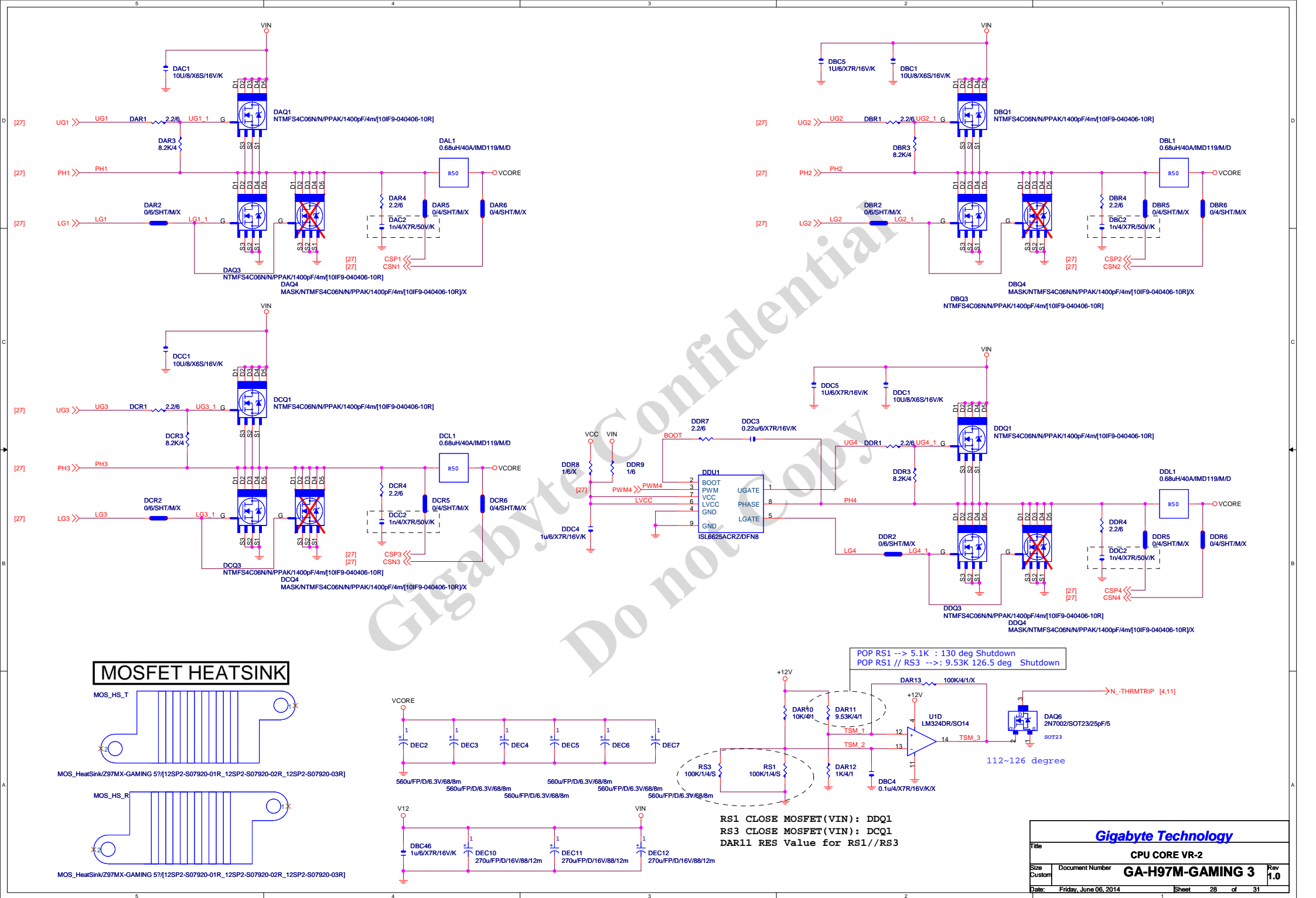


Gigabyte Technology

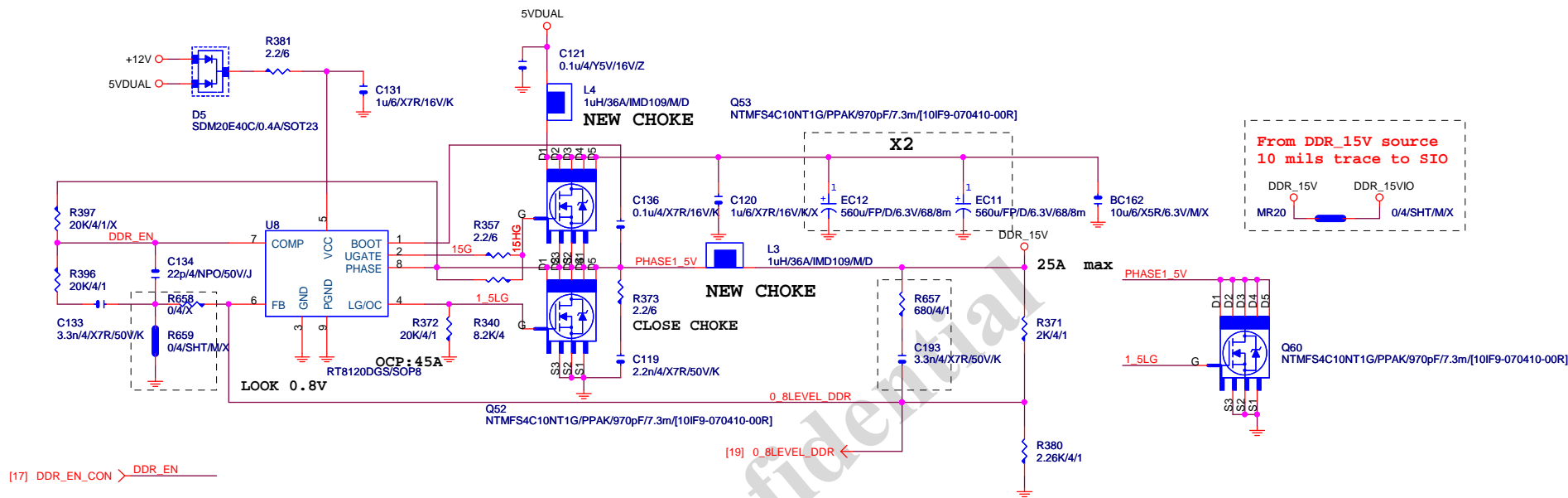
ATX CONNECTOR

GA-H97M-GAMING 3

Rev 1.0



DDR15V



PWR SEQ

VIN=5V, VOUT=1.5V, IOUT=25A, PHASE=1
 IRMS=11.45A
 560u/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A
 Coefficient=1.7(85°C), 1(105°C)
 VIN Ripple current=4.7X1.7=7.99A(85°C)
 -->故固態電容須2X7.99=15.98>11.45A

$$Rocset = (Iocp * Lgate, rdson) / Iocset$$

$$Rocset = (45A * 6.7m\Omega) / 10uA = 30K$$

$$Iocset = 10uA$$

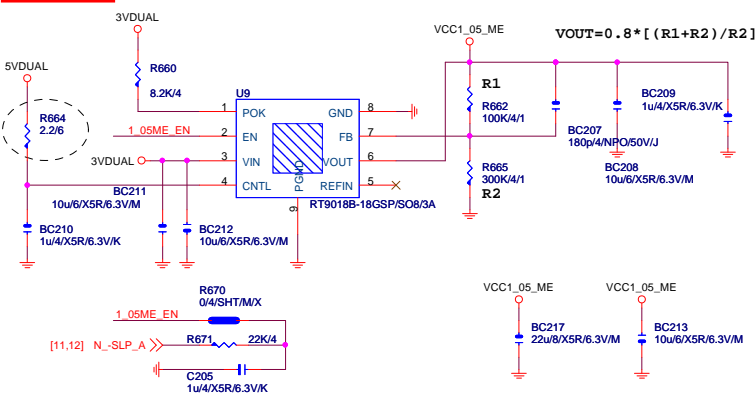
Gigabyte Technology

Title			
DDR POWER			
Size	Document Number	Rev	
Custom	GA-H97M-GAMING 3	1.0	
Date:	Friday, June 06, 2014	Sheet	29 of 31

VCC1_05_ME

【技術通報R&D技術通報156】
(RICHTER), (NUVOTON), (EMC) 做共用
PIN7分壓阻值須做修改為100K以上電阻值

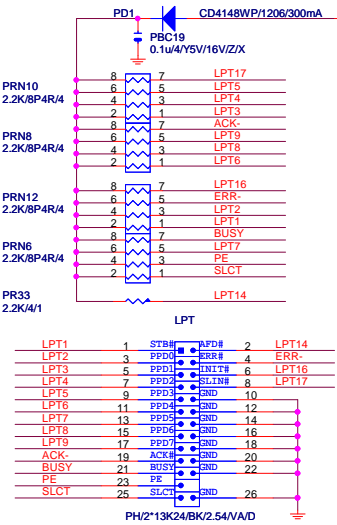
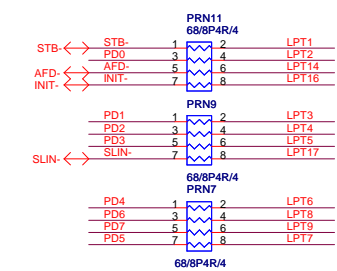
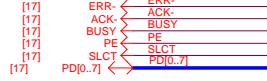
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Second source
EM5103 - 10GL2-305103-01R
NCT3730S -
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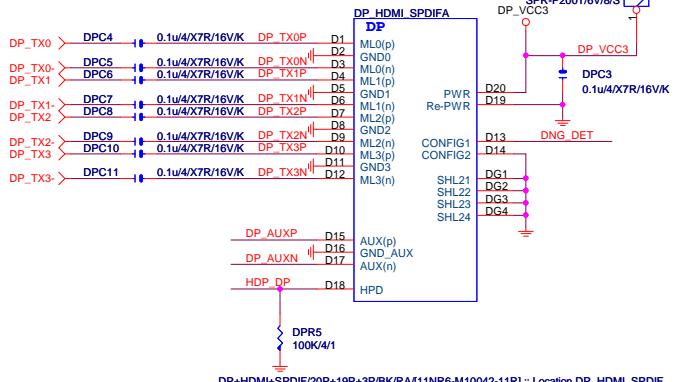
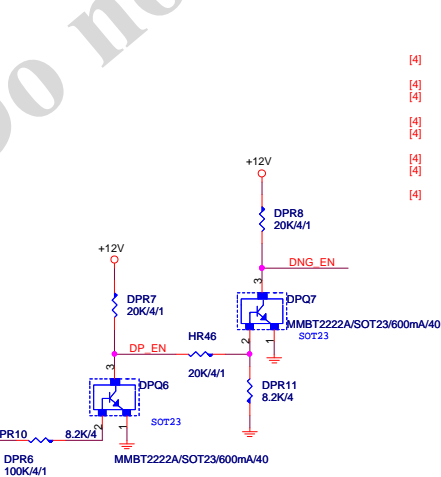
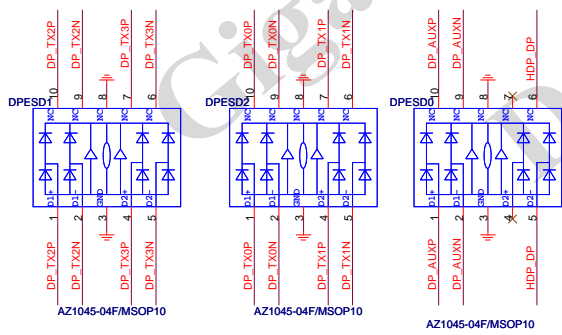
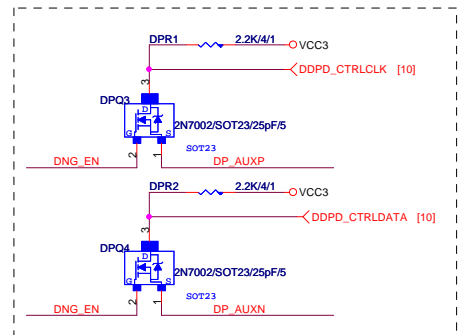
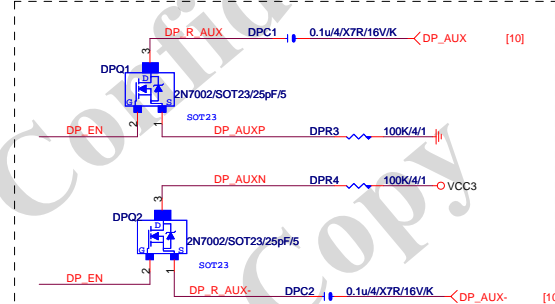
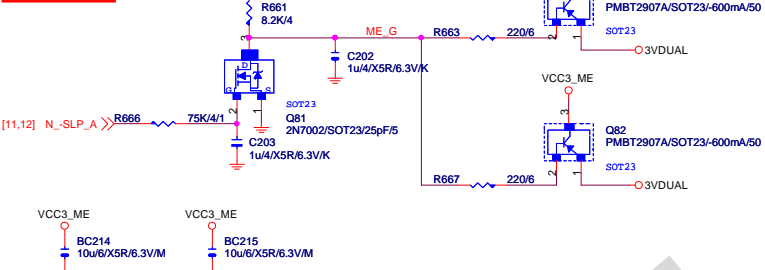
LPT PORT

【技術通報R&D技術通報151】
33ohm Change to 68ohm

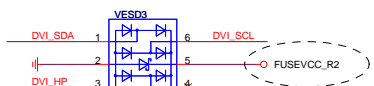
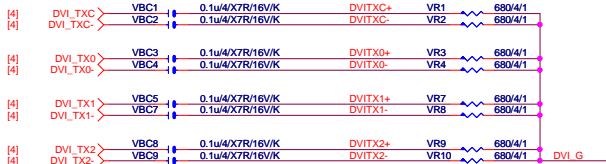


VCC3_ME

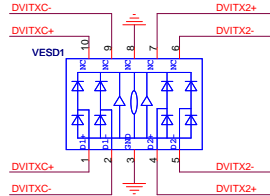
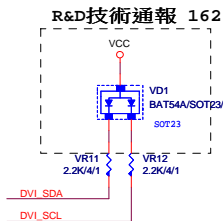
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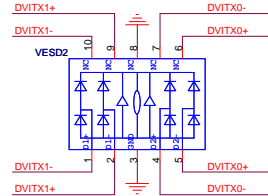
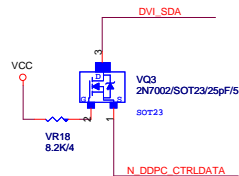
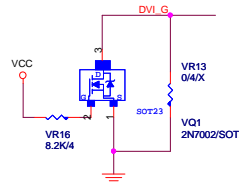
DVI LEVEL SHIFT



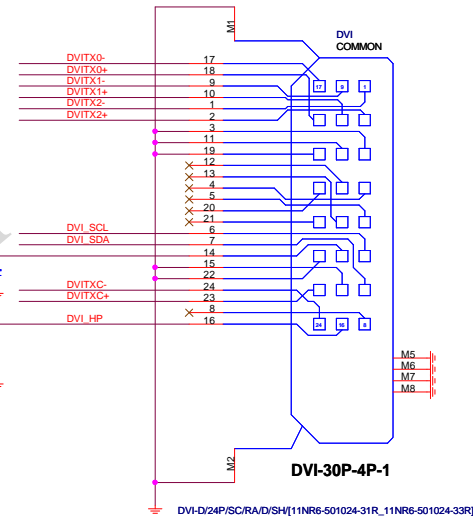
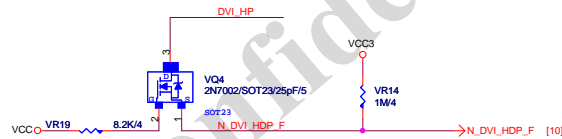
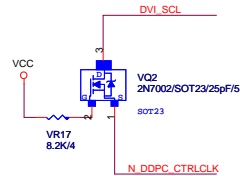
Close to connector



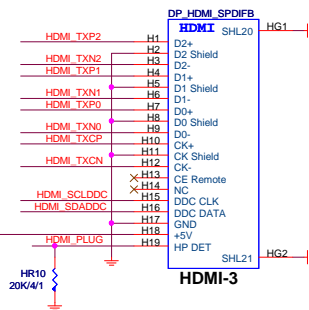
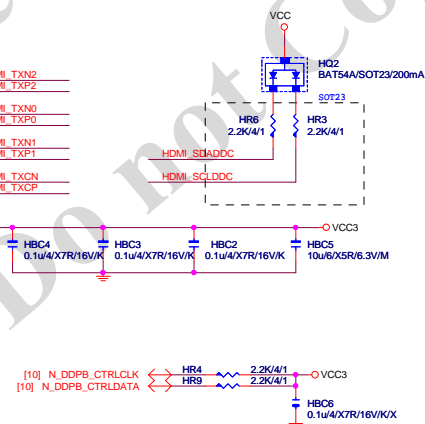
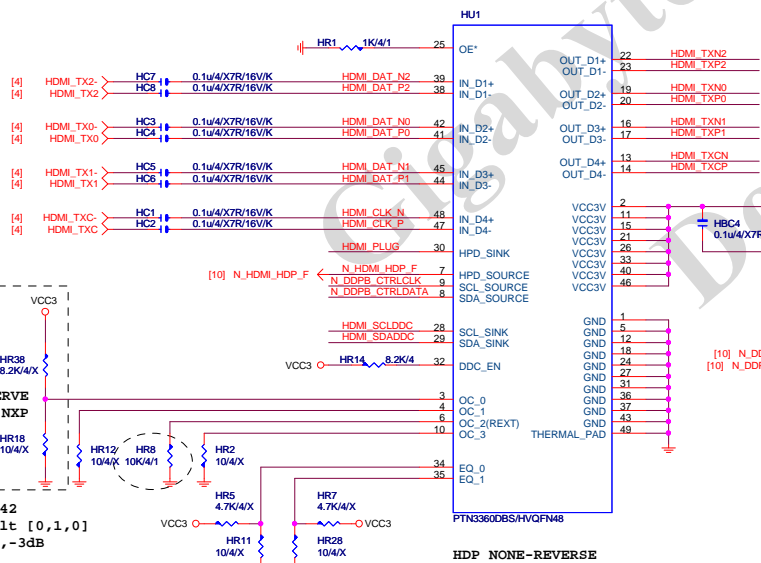
Close to connector



Close to connector



HDMI LEVEL SHIFT



DP+HDMI+SPDIF/20P+19P+3P/BK/RA/[11NR6-M10042-11R] :: Location DP_HDMI_SPDIF